

$\mathbb{H} \{CTL\}^{\star} \mathbb{H}$ CTL # family-based model checking using variability abstractions and modal transition systems

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CTL^{*} family-based model checking using variability abstractions and modal transition systems

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Abstract

Variational systems can produce a (potentially huge) number of related systems, known as products or variants, by using features (configuration options) to mark the variable functionality. In many of the application domains, their rigorous verification and analysis are very important, yet the appropriate tools rarely are able to analyse variational systems. Recently, this problem was addressed by designing specialized so-called family-based model checking algorithms, which allow simultaneous verification of all variants in a single run by exploiting the commonalities between the variants. Yet, their computational cost still greatly depends on the number of variants (the size of configuration space), which is often huge. Moreover, their implementation and maintenance represent a costly research and development task. One of the most promising approaches to fighting the configuration space explosion problem is *variability abstractions*, which simplify variability away from variational systems. In this work, we show how to achieve efficient family-based model checking of CTL^{*} temporal properties using variability abstractions and off-the-shelf (single-system) tools. We use variability abstractions for deriving abstract family-based model checking, where the variability model of a variational system is replaced with an abstract (smaller) version of it, called *modal transition system*, which preserves the satisfaction of both universal and existential temporal properties, as expressible in CTL^{*}. Modal transition systems contain two kinds of transitions, termed may- and must-transitions, which are defined by the conservative (over-approximating) abstractions and their dual (under-approximating) abstractions, respectively. The variability abstractions can be combined with different partitionings of the configuration space to infer suitable divide-and-conquer verification plans for the given variational system. We illustrate the practicality of this approach for several variational systems using the standard version of (single-system) NUSMV model checker.

Keywords Software product line engineering · Family-based model checking · Abstract interpretation · Modal transition systems · Featured transition systems · CTL^{*} temporal logic

1 Introduction

Variational systems appear in many application areas and for many reasons. Efficient methods to achieve customization, such as *software product line engineering* (SPLE) [12], use *features* (configuration options) to control presence and absence of the variable functionality [1]. Family members, called *variants* of a *variational system*, are specified in terms of features selected for that particular variant. The reuse of code common to multiple variants is maximized. The SPLE method is particularly popular in the embedded and criti-

cal system domains (e.g. cars, phones, avionics, health care) [12,26]. In these domains, a rigorous verification and analysis are very important. Indeed, engineers have to provide solid proofs that all variants satisfy their desired properties. Among the methods included in current practices, *model checking* [3] is a well-studied technique used to establish or refute that temporal logic properties hold for a system.

Despite numerous benefits of variability and SPLE, a growing amount of variability leads to combinatorial complexity and, consequently, to severe challenges. Obviously, the size of the configuration space (i.e. the number of variants) is the limiting factor to the feasibility of any verification technique. Exponentially, many variants can be derived from few configuration options. This problem is referred to as *the configuration space explosion* problem. A simple “brute-force” application of a single-system model checker to each

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variant is infeasible for realistic variational systems, due to the sheer number of variants. This is very ineffective also because the same execution behaviour is checked multiple times, whenever it is shared by several variants.

Researchers have addressed this problem by designing new more efficient verification techniques, which are based on using compact representations for modelling variational systems that incorporate the commonality within the family. We use the term *variability models* to denote such compact representations of variational systems. One of the most popular and widely accepted variability models today is *featured transition systems* (FTSs) [10]. Each behaviour in an FTS is associated with the set of variants able to produce it. A specialized family-based model checking algorithm [9,10] executed on such a model checks an execution behaviour only once regardless of how many variants include it. These algorithms model check all variants simultaneously in a single run and report precise conclusive results for all individual variants (whether they satisfy or violate a given property). Unfortunately, their performance *still* heavily depends on the size and complexity of the configuration space of the analysed variational system. Moreover, developing and maintaining specialized family-based tools is also an expensive task.

In order to address these challenges, we propose to use standard, single-system model checkers with an alternative, externalized way to combat the configuration space explosion. We apply the so-called *variability abstractions* to a variability model (FTS) which is too large to handle (“configuration space explosion”), producing a more *abstract model*, which is smaller than the original one. We abstract from certain aspects of the configuration space, so that many of the concrete configurations (variants) become indistinguishable and can be collapsed into a single abstract configuration (i.e. abstract variant). The abstract model is constructed in such a way that if some property holds for this abstract model, then it will also hold for the concrete variability model. The technique proposed in this paper significantly extends the scope of existing over-approximating variability abstractions introduced in [17,25], which support the verification of universal properties only (LTL and \forall CTL). More specifically, here we construct abstract variability models which can be used to check arbitrary formulae of CTL*, thus including arbitrary nested (universal and existential) path quantifiers. We use modal transition systems (MTSs) for representing abstract variability models. MTSs are transition systems (TSs) with two kinds of transitions, *must* and *may*, expressing behaviours that necessarily occur (must) or possibly occur (may) in the concrete model. We use the standard conservative (over-approximating) abstractions to define may-transitions, and their dual (under-approximating) abstractions to define must-transitions. Therefore, MTSs perform both over- and under-approximation, admitting both

universal and existential properties to be deduced. Since the constructed abstract models (given as MTSs) preserve satisfiability of all CTL* properties, if any such property is true for an abstract MTS, then it is also true for the concrete variability model (which is given as an FTS). Moreover, we show that any model checking problem on MTSs can be reduced to two traditional model checking problems on standard TSs. In particular, we show that a property holds on an MTS, if both its may- and must-parts (which represent standard TSs) satisfy that property. The overall verification technique relies on partitioning the configuration space and abstracting separately concrete FTSs corresponding to individual partitions, until the point we obtain abstract models with no variability, so that it is feasible to complete their model checking in the brute-force fashion using the standard single-system model checkers.

The main goal of abstraction is to avoid the construction of the full concrete model. Hence, we want to derive an abstract model directly from some high-level description of the variational system. We show how partitionings and abstractions of variational systems specified in a high-level modelling language (which is an input language for the NUSMV model checker) can be implemented as simple preprocessor transformations. In this way, we do not perform any modifications to the model checker. Although the proposed technique is theoretically designed and shown correct for verifying arbitrarily CTL* properties, our implementation is based on the NUSMV model checker and CTL properties. This is due to the fact that we want to compare our technique with the traditional CTL family-based model checking algorithm (used as a baseline) [11], which is based on an extended version of NUSMV model checker and CTL properties. Experiments show that our technique combined with the standard version of NUSMV achieves considerable performance gains.

We make the following contributions here:

- Conservative and their dual *variability abstractions* for featured transition systems are defined.
- *Modal transition systems* for representing abstract variability models are introduced, and their soundness with respect to CTL* properties is shown.
- *Abstractions are implemented as syntactic transformations* of variational systems specified in a high-level modelling language in a preprocessing step, without any modifications to the model checking tools.
- *Efficient CTL family-based model checking* technique using an off-the-shelf model checker NUSMV is proposed.
- *Evaluation* of the above technique for CTL family-based model checking with NUSMV is presented, which shows scalability gains against the traditional CTL family-based model checking algorithms and against brute-force enumeration approach.

This work is an extended and revised version of [16]. We revise and correct the syntax and semantics of CTL* formulae in negation normal form given in [16]. We also make the following extensions here: (1) We motivate the need for using over- and under-approximating variability abstractions for achieving efficient CTL* family-based model checking; (2) we provide formal proofs for all main results in the work; (3) we expand and elaborate the examples as well as the discussion on how this approach works; (4) we provide more details on how may- and must-parts of high-level abstract models are defined as source-to-source transformations; (5) we greatly augment the evaluation of this approach by defining precise research questions, including new case-study models, considering more properties, and extending performance results.

We proceed with a motivating example for using variability abstractions for CTL* family-based model checking in Sect. 2. The basics of CTL* family-based model checking are explained in Sect. 3. Section 4 defines variability abstractions as well as abstract variability models and proves that they preserve the CTL* properties. Section 5 explains how to encode variational systems using high-level modelling languages and presents how abstractions are implemented as source-to-source transformations of such high-level models. The evaluation on several case studies is presented in Sect. 6. In Sect. 7, we show how our verification procedure can be extended in order to (1) handle μ -calculus properties and (2) become a fully automatic procedure. Finally, we discuss the relation to other works and conclude.

2 Motivating example

A variability model for the VENDINGMACHINE [10] variational system is shown in Fig. 1. It describes the behaviours of a family of models of vending machines in an aggregating form using a featured transition system [10]. The VENDINGMACHINE family has five features, and each of them is assigned an identifying letter and a color. The features are: VendingMachine (denoted by v , in black), the mandatory base feature for purchasing a drink which is enabled in all variants of this family; Tea (t , in red), for serving tea; Soda (s , in green), for serving soda, which is also a mandatory feature present in all variants; CancelPurchase (c ,

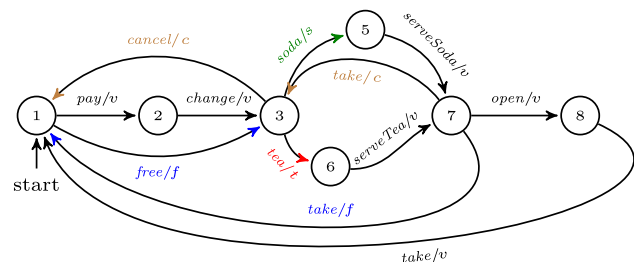
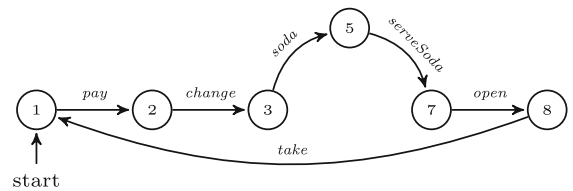
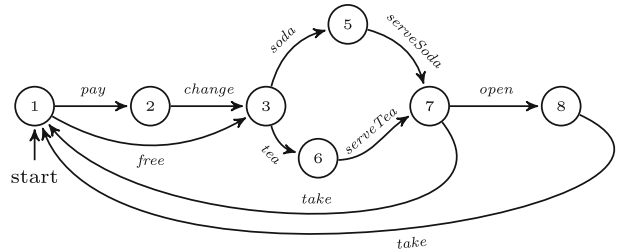


Fig. 1 The variability model for VENDINGMACHINE



(a) The variant for configuration $\{v, s\}$.



(b) The variant for configuration $\{v, s, t, f\}$.

Fig. 2 Some variants of VENDINGMACHINE

in brown), for cancelling a purchase after a coin is entered; and FreeDrinks (f , in blue) for offering free drinks. Each transition is labelled by first an *action*, then a slash '/', and a guarding *feature expression* specifying for which variants the transition is to be included (this depends on which features have been enabled in a given variant). For example, the transition $\textcircled{1} \xrightarrow{\text{free}/f} \textcircled{3}$ is included in variants that have the feature f enabled.

By combining various features, a number of variants of the VENDINGMACHINE family can be derived. Figure 2a shows a basic variant of VENDINGMACHINE that only serves soda. Only the features v and s are enabled, so it is described by the configuration $\{v, s\}$. The machine takes a coin, returns change, serves a soda, opens the access compartment so that the customer can take the soda, before closing it again. Figure 2b shows another variant of this machine (features v, s, t , and f enabled), which serves both tea and soda, and offers free drinks as well as paid drinks. Other variants can be derived by enabling other combinations of features. We can derive up to 2^n variants, where n is the number of features available in the family. In general, not all combinations of features give rise to *valid* variants (configurations). For the VENDINGMACHINE family, the features v and s are mandatory, so they must be enabled in all valid variants. The other features t, c , and f are optional, thus yielding eight valid variants in this family.

Suppose that a proposition *start* holds in the initial state $\textcircled{1}$. Consider the following property

Φ_1 : in every state along every execution, there exists a possible continuation that will eventually reach the start state.

Both variants of VENDINGMACHINE in Fig. 2 satisfy this property, since the initial state $\textcircled{1}$ is reachable from any state

of those variants. In fact, all variants of VENDINGMACHINE satisfy Φ_1 .

Model checking [3] can be used to verify formally whether properties like Φ_1 hold for the VENDINGMACHINE family. One possibility is to instantiate all valid variants of the family and verify them one by one, using a standard single-system model checker (this is known as “brute-force” approach). Alternatively, we can use a specialized family-based model checking algorithms and tools [11] that operate directly on the variability model. Although the family-based model checkers are more efficient than the brute-force approach, their efficiency still depends on the number of features and variants in the family. Moreover, family-based model checkers require a costly design and implementation.

In order to overcome the above problems of family-based approach (scalability and development cost), we previously proposed to use *variability abstractions* [17, 18]. They reduce the number of variants, producing abstract variability models which are smaller than the concrete ones. Hence, the model checking of these models is computationally more efficient, but less precise. However, the variability abstractions introduced in [17, 18] are conservative, which means that the resulting abstract variability models over-approximate the concrete ones and are sound only with respect to universal properties (that contain just the universal \forall path quantifier.) On the other hand, the above property Φ_1 is not universal, since it contains both universal quantifiers (for all executions, \forall) and existential quantifiers (there exists an execution, \exists). In order to handle such properties, we have to use both conservative (over-approximating) variability abstractions and their dual (under-approximating) variability abstractions. In effect, we will obtain an abstract variability model (known as modal transition system), which has two types of transitions: *may*-transitions defined using the conservative abstractions, and *must*-transitions defined using the dual abstractions. The property Φ_1 is now interpreted over abstract variability models as follows:

Φ_1 : *in every state along every may-execution (that contains only may-transitions), there exists a possible must-continuation (that contains only must-transitions) that will eventually reach the start state.*

If the property holds in such an abstract variability model, then it will hold in the concrete variability model as well (by our soundness result).

We consider a basic variability abstraction, called the *join abstraction* (written α^{join}). It merges all valid variants into a single abstract model, such that its may-part contains all transitions that occur in at least one variant, whereas its must-part contains only those transitions that occur in all variants. Note that with α^{join} we obtain a single-system model with no variability in it. We combine the simplifica-

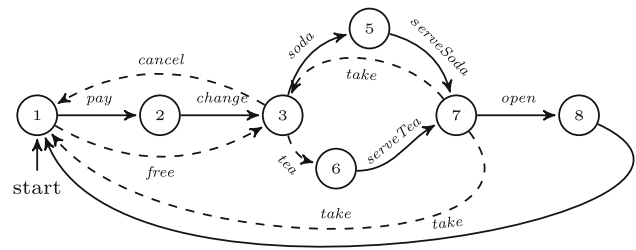


Fig. 3 The abstract model α^{join} (VENDINGMACHINE). Dashed edges represent may-transitions, while solid edges represent must-transitions

tion of concrete models by the variability abstraction with a divide-and-conquer strategy for more efficient verification. The key operator for this strategy is *projection* (denoted π), which can be used to partition the configuration space of all variants into disjoint subsets, producing several concrete variability models that can be analysed (and abstracted) separately.

We illustrate the use of abstractions via our example property Φ_1 . We apply the α^{join} abstraction on the variability model of Fig. 1, which simply joins control flows of all variants into a single model (known as modal transition system), where all (mandatory and optional) features become *true* in may-transitions and only mandatory features become *true* in must-transitions (the other optional features become *false*). As a result of this operation, we obtain the abstract model α^{join} (VENDINGMACHINE), shown in Fig. 3, where may-transitions are denoted by dashed lines and must-transitions are denoted by solid lines. Note that every must-transition is also a may-transition, and we only show those (may and must) transitions whose presence conditions have evaluated to *true* after applying the join abstraction. The may-part of α^{join} (VENDINGMACHINE), which contains only may-executions, over-approximates the VENDINGMACHINE in the sense that it contains more executions than VENDINGMACHINE. On the other hand, the must-part of α^{join} (VENDINGMACHINE), which contains only must-executions, under-approximates the VENDINGMACHINE in the sense that it contains less executions than VENDINGMACHINE. The variability-specific information about features is lost in the abstract model. The may- and must-parts of α^{join} (VENDINGMACHINE) represent ordinary transition systems, and they can be verified efficiently using standard (single-system) model checkers (such as NUSMV). It can be shown that an abstract variability model satisfies one property, if both its may- and must-parts satisfy the same property. Thus, by verifying that Φ_1 holds for the may- and must-parts of α^{join} (VENDINGMACHINE), we have that Φ_1 holds for α^{join} (VENDINGMACHINE). Then, using the soundness result for the join abstraction we can conclude that Φ_1 holds for the concrete VENDINGMACHINE as well (i.e. for all its valid variants).

3 Background

We begin by summarizing the existing background for our work. We define modelling formalisms for describing single systems and then proceed with modelling formalisms for variational systems. Finally, we present the temporal logic CTL*, which is used to specify system properties.

3.1 Single systems

We present the basic definition of a transition system (TS) and a modal transition system (MTS) that we will use to describe behaviours of single systems.

Definition 1 A transition system (TS) is a tuple $\mathcal{T} = (S, Act, trans, I, AP, L)$, where S is a finite set of states; Act is a finite set of actions; $trans \subseteq S \times Act \times S$ is a transition relation which is *total*, so that for each state there is an outgoing transition; $I \subseteq S$ is a set of initial states; AP is a set of atomic propositions; and $L : S \rightarrow 2^{AP}$ is a labelling function specifying which propositions hold in a state. We write $s_1 \xrightarrow{\lambda} s_2$ whenever $(s_1, \lambda, s_2) \in trans$.

An *execution* (behaviour) of a TS \mathcal{T} is an *infinite* sequence $\rho = s_0 \lambda_1 s_1 \lambda_2 \dots$ with $s_0 \in I$ such that $s_i \xrightarrow{\lambda_{i+1}} s_{i+1}$ for all $i \geq 0$. The *semantics* of the TS \mathcal{T} , denoted as $\llbracket \mathcal{T} \rrbracket_{TS}$, is the set of its executions.

Remark TSs are used for modelling reactive systems whose computations typically do not terminate. In such systems, terminal states in which no progress is possible are undesirable and often represent a design error. Therefore, we consider TSs without terminal states (transition relation is total) and only infinite sequences.

MTSs [35] are a generalization of transition systems that allows describing not just a sum of all behaviours of a system but also an over- and under-approximation of the system's behaviours. An MTS is a TS equipped with two transition relations: *must* and *may*. The former (must) is used to specify the required behaviour, while the latter (may) to specify the allowed behaviour of a system. We will use MTSs for representing abstractions of variational systems.

Definition 2 A modal transition system (MTS) is a tuple $\mathcal{M} = (S, Act, trans^{may}, trans^{must}, I, AP, L)$, where $trans^{may} \subseteq S \times Act \times S$ describe may-transitions of \mathcal{M} ; $trans^{must} \subseteq S \times Act \times S$ describe must-transitions of \mathcal{M} , such that $trans^{may}$ is total and $trans^{must} \subseteq trans^{may}$.

The intuition behind the inclusion $trans^{must} \subseteq trans^{may}$ is that transitions that are necessarily true ($trans^{must}$) are also possibly true ($trans^{may}$). A *may-execution* in \mathcal{M} is an execution (infinite sequence) with all its transitions in $trans^{may}$, whereas a *must-execution* in \mathcal{M} is a maximal sequence with

all its transitions in $trans^{must}$, which cannot be extended with any other transition from $trans^{must}$. Note that since $trans^{must}$ is not necessarily total, must-executions can be finite. We use $\llbracket \mathcal{M} \rrbracket_{MTS}^{may}$ to denote the set of all may-executions in \mathcal{M} , whereas $\llbracket \mathcal{M} \rrbracket_{MTS}^{must}$ to denote the set of all must-executions in \mathcal{M} .

3.2 Variational systems

Let $\mathbb{F} = \{A_1, \dots, A_n\}$ be a finite set of Boolean variables representing the features available in a variational system. A specific subset of features, $k \subseteq \mathbb{F}$, known as *configuration*, specifies a *variant* (valid product) of a variational system. We assume that only a subset $\mathbb{K} \subseteq 2^{\mathbb{F}}$ of configurations are *valid*. An alternative representation of configurations is based upon propositional formulae. Each configuration $k \in \mathbb{K}$ can be represented by a formula: $k(A_1) \wedge \dots \wedge k(A_n)$, where $k(A_i) = A_i$ if $A_i \in k$, and $k(A_i) = \neg A_i$ if $A_i \notin k$ for $1 \leq i \leq n$. We will use both representations interchangeably.

An FTS describes behaviour of a whole family of systems in a *superimposed* manner. This means that it combines models of many variants in a single monolithic description, where the transitions are guarded by a *presence condition* that identifies the variants they belong to. The presence conditions ψ are drawn from the set of feature expressions, $FeatExp(\mathbb{F})$, which are propositional logic formulae over \mathbb{F} :

$$\psi ::= true \mid A \in \mathbb{F} \mid \neg \psi \mid \psi_1 \wedge \psi_2$$

The presence condition ψ of a transition specifies the variants in which the transition is enabled. We write $\llbracket \psi \rrbracket$ to denote the set of variants from \mathbb{K} that satisfy ψ , i.e. $k \in \llbracket \psi \rrbracket$ iff $k \models \psi$, where \models is the standard satisfaction relation of propositional logic. For example, given $\mathbb{F} = \{A, B\}$ with all four possible variants being valid, we get: $\llbracket A \vee B \rrbracket = \{A \wedge B, A \wedge \neg B, \neg A \wedge B\}$.

Definition 3 A featured transition system (FTS) represents a tuple $\mathcal{F} = (S, Act, trans, I, AP, L, \mathbb{F}, \mathbb{K}, \delta)$, where $S, Act, trans, I, AP,$ and L are defined as in TS; \mathbb{F} is the set of available features; \mathbb{K} is a set of valid configurations; and $\delta : trans \rightarrow FeatExp(\mathbb{F})$ is a total function decorating transitions with presence conditions (feature expressions).

The *projection* of an FTS \mathcal{F} to a variant $k \in \mathbb{K}$, denoted as $\pi_k(\mathcal{F})$, is the TS $(S, Act, trans', I, AP, L)$, where $trans' = \{t \in trans \mid k \models \delta(t)\}$. We lift the definition of *projection* to sets of configurations $\mathbb{K}' \subseteq \mathbb{K}$, denoted as $\pi_{\mathbb{K}'}(\mathcal{F})$, by keeping the transitions admitted by at least one of the configurations in \mathbb{K}' . That is, $\pi_{\mathbb{K}'}(\mathcal{F})$, is the FTS $(S, Act, trans', I, AP, L, \mathbb{F}, \mathbb{K}', \delta')$, where $trans' = \{t \in trans \mid \exists k \in \mathbb{K}'. k \models \delta(t)\}$ and $\delta' = \delta|_{trans'}$ is the restriction of δ to $trans'$. The *semantics* of an FTS \mathcal{F} , denoted as

$\llbracket \mathcal{F} \rrbracket_{\text{FTS}}$, is the union of behaviours of the projections on all valid variants $k \in \mathbb{K}$, i.e. $\llbracket \mathcal{F} \rrbracket_{\text{FTS}} = \cup_{k \in \mathbb{K}} \llbracket \pi_k(\mathcal{F}) \rrbracket_{\text{TS}}$.

Example 1 Consider the FTS for the VENDINGMACHINE family presented in Fig. 1. The FTS has five features $\mathbb{F} = \{v, t, s, c, f\}$. The set of all valid configurations is obtained by combining the above features. Recall that v and s are mandatory features, so the set of valid configurations is: $\mathbb{K}^{\text{VM}} = \{\{v, s\}, \{v, s, t\}, \{v, s, c\}, \{v, s, t, c\}, \{v, s, f\}, \{v, s, t, f\}, \{v, s, c, f\}, \{v, s, t, c, f\}\}$.

Figure 2a shows a basic version of VENDINGMACHINE that only serves soda. This variant is described by the configuration: $\{v, s\}$, equivalently $v \wedge s \wedge \neg t \wedge \neg c \wedge \neg f$. The model presented in the figure is obtained by the projection $\pi_{\{v, s\}}(\text{VENDINGMACHINE})$. Similarly, we can obtain the model $\pi_{\{v, s, t, f\}}(\text{VENDINGMACHINE})$ shown in Fig. 2b.

Figure 3 shows an MTS, where must-transitions are denoted by solid lines and may-transitions are denoted by dashed lines. \square

3.3 CTL* properties

Computation tree logic* (CTL*) [3,7] is an expressive temporal logic for specifying system properties, which subsumes both CTL and LTL logics. CTL* state formulae Φ are generated by the following grammar:

$$\begin{aligned} \Phi ::= & \text{true} \mid \text{false} \mid a \mid \neg a \mid \Phi_1 \wedge \Phi_2 \mid \Phi_1 \vee \Phi_2 \mid \forall \phi \mid \exists \phi, \\ \phi ::= & \Phi \mid \phi_1 \wedge \phi_2 \mid \phi_1 \vee \phi_2 \mid \bigcirc \phi \mid \phi_1 \text{U} \phi_2 \mid \phi_1 \text{V} \phi_2 \end{aligned}$$

where $a \in AP$ and ϕ represent CTL* path formulae. The path formula $\bigcirc \phi$ can be read as “from the next state ϕ ”, while $\phi_1 \text{U} \phi_2$ can be read as “ ϕ_1 until ϕ_2 ”, and $\phi_1 \text{V} \phi_2$ can be read as “ ϕ_2 while not ϕ_1 ” (where ϕ_1 may never hold). Other derived temporal operators for path formulae can be defined as well by means of syntactic sugar, for instance: $\diamond \phi = \text{true} \text{U} \phi$ (ϕ holds eventually), and $\square \phi = \neg \diamond \neg \phi$ (ϕ always holds). Note that the CTL* state formulae Φ are given in negation normal form (\neg is applied only to atomic propositions). This facilitates the definition of $\forall \text{CTL}^*$ and $\exists \text{CTL}^*$, which are subsets of CTL* where the only allowed path quantifiers are \forall and \exists , respectively. Given $\Phi \in \text{CTL}^*$, we consider $\neg \Phi$ to be the equivalent CTL* formula given in negation normal form. To ensure that every CTL* formula is equivalent to a formula in negation normal form, for each operator the corresponding dual operator is necessary. We have that \wedge and \vee are dual, \bigcirc is dual to itself, as well as U and V are dual. For example, we use the duality law: $\neg \forall \bigcirc \Phi \equiv \exists \bigcirc \neg \Phi$.

We formalize the semantics of CTL* over a TS \mathcal{T} . We write $\llbracket \mathcal{T} \rrbracket_{\text{TS}}^s$ for the set of executions that start in state s ; $\rho[i] = s_i$ to denote the i th state of the execution ρ ; and $\rho_i = s_i \lambda_{i+1} s_{i+1} \dots$ for the suffix of ρ starting from its i th state.

Definition 4 Satisfaction of a state formula Φ in a state s of a TS \mathcal{T} , denoted $\mathcal{T}, s \models \Phi$, is defined as (\mathcal{T} is omitted when clear from context):

- (1) $s \models a$ iff $a \in L(s)$; $s \models \neg a$ iff $a \notin L(s)$,
- (2) $s \models \Phi_1 \wedge \Phi_2$ iff $s \models \Phi_1$ and $s \models \Phi_2$;
 $s \models \Phi_1 \vee \Phi_2$ iff $s \models \Phi_1$ or $s \models \Phi_2$
- (3) $s \models \forall \phi$ iff $\forall \rho \in \llbracket \mathcal{T} \rrbracket_{\text{TS}}^s. \rho \models \phi$;
 $s \models \exists \phi$ iff $\exists \rho \in \llbracket \mathcal{T} \rrbracket_{\text{TS}}^s. \rho \models \phi$

Satisfaction of a path formula ϕ for an execution $\rho = s_0 \lambda_1 s_1 \dots$ of a TS \mathcal{T} , denoted $\mathcal{T}, \rho \models \phi$, is defined as (\mathcal{T} is omitted when clear from context):

- (4) $\rho \models \Phi$ iff $\rho[0] \models \Phi$,
- (5) $\rho \models \phi_1 \wedge \phi_2$ iff $\rho \models \phi_1$ and $\rho \models \phi_2$;
 $\rho \models \phi_1 \vee \phi_2$ iff $\rho \models \phi_1$ or $\rho \models \phi_2$;
 $\rho \models \bigcirc \phi$ iff $\rho_1 \models \phi$;
 $\rho \models (\phi_1 \text{U} \phi_2)$ iff $\exists i \geq 0. (\rho_i \models \phi_2 \wedge (\forall 0 \leq j < i. \rho_j \models \phi_1))$
 $\rho \models (\phi_1 \text{V} \phi_2)$ iff $\forall i \geq 0. (\forall 0 \leq j < i. \rho_j \not\models \phi_1 \implies \rho_i \models \phi_2)$

A TS \mathcal{T} satisfies a state formula Φ , written $\mathcal{T} \models \Phi$, iff all its initial states satisfy the formula: $\forall s_0 \in I. s_0 \models \Phi$.

We say that an FTS \mathcal{F} satisfies a CTL* formula Φ , written $\mathcal{F} \models \Phi$, iff all its valid variants satisfy the formula: $\forall k \in \mathbb{K}. \pi_k(\mathcal{F}) \models \Phi$. Otherwise, we say that \mathcal{F} does not satisfy Φ , written $\mathcal{F} \not\models \Phi$. In this case, we also want to determine a non-empty set of violating variants $\mathbb{K}' \subseteq \mathbb{K}$, such that $\forall k' \in \mathbb{K}'. \pi_{k'}(\mathcal{F}) \not\models \Phi$ and $\forall k \in \mathbb{K} \setminus \mathbb{K}'. \pi_k(\mathcal{F}) \models \Phi$.

We now define the semantics of CTL* over an MTS \mathcal{M} . We define $\mathcal{M}, s \models \Phi$ and $\mathcal{M}, \rho \models \phi$, which are slightly different from Definition 4 where a TS \mathcal{T} is considered. In particular, the clause (3) is replaced by:

- (3') $\mathcal{M}, s \models \forall \phi$ iff for every may-execution ρ in the state s of \mathcal{M} , that is, $\forall \rho \in \llbracket \mathcal{M} \rrbracket_{\text{MTS}}^{\text{may}, s}$, it holds $\rho \models \phi$;
 $\mathcal{M}, s \models \exists \phi$ iff there exists a must-execution ρ in the state s of \mathcal{M} , that is, $\exists \rho \in \llbracket \mathcal{M} \rrbracket_{\text{MTS}}^{\text{must}, s}$, such that $\rho \models \phi$.

An MTS \mathcal{M} satisfies a state formula Φ , written $\mathcal{M} \models \Phi$, iff $\forall s_0 \in I. s_0 \models \Phi$.

Note that the duality laws no longer hold for MTSs. However, we use MTSs as abstract variability models that only help to speed up the procedure for verifying CTL* properties over FTSs (i.e. a family of TSs).

Example 2 Consider the FTS VENDINGMACHINE in Fig. 1. We restate the example property Φ_1 from Sect. 2 in CTL* as follows:

$$\Phi_1 = \forall \square \exists \diamond \text{start}$$

where the proposition `start` holds in the initial state ①. The property states that in every state along every execution there exists a possible continuation that will eventually reach the `start` state. This is a CTL* formula, which is neither in $\forall\text{CTL}^*$ nor in $\exists\text{CTL}^*$. Note that $\text{VENDINGMACHINE} \models \Phi_1$, since Φ_1 holds for all variants. Even for variants with the feature c enabled, there is a continuation from the state ⑤ back to ①.

Consider another property

$$\Phi_2 = \forall \square \forall \diamond \text{start}$$

It states that in every state along every execution all possible continuations will eventually reach the initial state. This formula is in $\forall\text{CTL}^*$. Note that $\text{VENDINGMACHINE} \not\models \Phi_2$. For example, if the feature c (Cancel) is enabled, a counter-example where the state ① is never reached is: ① \rightarrow ③ \rightarrow ⑤ \rightarrow ⑦ \rightarrow ③ \rightarrow ... The set of violating products is $\llbracket c \rrbracket = \{\{v, s, c\}, \{v, s, t, c\}, \{v, s, c, f\}, \{v, s, t, c, f\}\} \subseteq \mathbb{K}^{\text{VM}}$. However, note that the variants from $\llbracket \neg c \rrbracket$ do satisfy Φ_2 , that is, $\pi_{\llbracket \neg c \rrbracket}(\text{VENDINGMACHINE}) \models \Phi_2$.

Finally, consider the $\exists\text{CTL}^*$ property

$$\Phi_3 = \exists \square \exists \diamond \text{start}$$

It states that there exists an execution such that in every state along it there exists a possible continuation that will eventually reach the `start` state. The witness is ① \rightarrow ② \rightarrow ③ \rightarrow ⑤ \rightarrow ⑦ \rightarrow ⑧ \rightarrow ① ..., which is an execution that belongs to all valid variants. \square

CTL* extends CTL as it allows path quantifiers \forall and \exists to be arbitrarily nested with temporal operators, such as \square and U . In contrast, in CTL each temporal operator must be immediately preceded by a path quantifier. Hence, all properties in Example 2 are from CTL. We now show how to handle a CTL* property which is not in CTL.

Example 3 Let the proposition `selectedTea` holds only in the state ⑥, and it does not hold in all other states. We want to check the property:

$$\Phi_4 = \forall \diamond \square (\neg \text{selectedTea})$$

It states that along every execution there exists a state such that after that state `selectedTea` does not hold forever. Note that $\text{VENDINGMACHINE} \not\models \Phi_4$. For example, if the feature t (Tea) is enabled, a possible counter-example is: ① \rightarrow ③ \rightarrow ⑥ \rightarrow ⑦ \rightarrow ① \rightarrow ... The set of violating products is $\llbracket t \rrbracket$. However, note that the variants from $\llbracket \neg t \rrbracket$ do satisfy Φ_4 , that is, $\pi_{\llbracket \neg t \rrbracket}(\text{VENDINGMACHINE}) \models \Phi_4$. \square

4 Abstraction of variational systems

We now introduce the variability abstractions which preserve full CTL*, including its universal and existential properties. The abstractions simplify the configuration space of a variational system (i.e. the corresponding FTS), by reducing the number of configurations and manipulating presence conditions of transitions. We start working with variability abstractions between Boolean complete lattices of feature expressions and then induce a notion of abstract models of FTSs. Finally, we show that the obtained abstract models are sound with respect to CTL* properties, and illustrate how those abstract models can be combined with a divide-and-conquer verification strategy based on partitioning the configuration space.

4.1 Variability abstractions

An *abstraction* is a mapping of elements in a concrete domain (say concrete models) to elements of an abstract domain (here the abstract models). Usually, we want to simplify representation of the object under analysis to speed up analysis. In program analysis and verification, domains are usually complete lattices, which have sufficiently rich structure that facilitates ordering of elements (some elements are more informative than others) and synthesizing new elements using least upper bound.

Let $\langle L, \leq_L \rangle$ and $\langle M, \leq_M \rangle$ be complete lattices taking the role of the *concrete* and *abstract* domain, respectively. A *Galois connection* is a pair of total functions, $\alpha : L \rightarrow M$ and $\gamma : M \rightarrow L$ (respectively, known as the *abstraction* and *concretization* functions) such that:

$$\alpha(l) \leq_M m \iff l \leq_L \gamma(m) \text{ for all } l \in L, m \in M.$$

We write $\langle L, \leq_L \rangle \xleftrightarrow[\alpha]{\gamma} \langle M, \leq_M \rangle$ to state that (α, γ) are a Galois connection between L and M . We will use Galois connections to approximate a computationally expensive (or uncomputable) analysis (model) formulated over L with a computationally cheaper analysis (model) formulated over M .

Variability abstractions simplify the configuration space of an FTS, by reducing the number of configurations. This is easy to do by manipulating presence conditions of transitions. Thus, we define abstraction of variability in FTSs primarily by abstraction of presence conditions, working with Galois connections between standard Boolean complete lattices. We define two classes of abstractions. We use the standard conservative abstractions [17,18] as an instrument to eliminate variability from the FTS in an *over-approximating* way, so by adding more executions. We use the dual abstractions, which can also eliminate variability but through *under-approximating* the given FTS, so by dropping executions.

Domains The Boolean complete lattice of feature expressions (propositional formulae over \mathbb{F}) is defined as: $(FeatExp(\mathbb{F})_{/\equiv}, \models, \vee, \wedge, true, false, \neg)$. The elements of the domain $FeatExp(\mathbb{F})_{/\equiv}$ are equivalence classes of propositional formulae $\psi \in FeatExp(\mathbb{F})$ obtained by quotienting by the semantic equivalence \equiv . The ordering \models is the standard entailment between propositional logics formulae, whereas the least upper bound and the greatest lower bound are just logical disjunction and conjunction, respectively. Finally, the constant *false* is the least, *true* is the greatest element, and negation is the complement operator.

Conservative abstractions The *join abstraction*, α^{join} , merges the control flow of all variants, obtaining a single variant that includes all executions that may occur in any variant. The information about which transitions are associated with which variants is lost. Each feature expression ψ is replaced with *true* if there exists at least one configuration from \mathbb{K} that satisfies ψ . The new abstract set of features is empty: $\alpha^{join}(\mathbb{F}) = \emptyset$, and the abstract set of valid configurations is a singleton: $\alpha^{join}(\mathbb{K}) = \{true\}$ if $\mathbb{K} \neq \emptyset$. The abstraction and concretization functions between $FeatExp(\mathbb{F})$ and $FeatExp(\emptyset)$, forming a Galois connection [17, 18], are defined as:

$$\alpha^{join}(\psi) = \begin{cases} true & \text{if } \exists k \in \mathbb{K}. k \models \psi \\ false & \text{otherwise} \end{cases} \quad (1)$$

$$\gamma^{join}(\psi) = \begin{cases} true & \text{if } \psi \text{ is } true \\ \bigvee_{k \in 2^{\mathbb{F}} \setminus \mathbb{K}} k & \text{if } \psi \text{ is } false \end{cases}$$

Dual abstractions. Suppose that $(FeatExp(\mathbb{F})_{/\equiv}, \models)$, $(FeatExp(\alpha(\mathbb{F}))_{/\equiv}, \models)$ are Boolean complete lattices, and $(FeatExp(\mathbb{F})_{/\equiv}, \models) \xrightarrow{\gamma} (FeatExp(\alpha(\mathbb{F}))_{/\equiv}, \models)$ is a Galois connection. We define [15]: $\tilde{\alpha} = \neg \circ \alpha \circ \neg$ and $\tilde{\gamma} = \neg \circ \gamma \circ \neg$ so that $(FeatExp(\mathbb{F})_{/\equiv}, \models) \xrightarrow{\tilde{\gamma}} (FeatExp(\alpha(\mathbb{F}))_{/\equiv}, \models)$ is a Galois connection (or equivalently, we have that $(FeatExp(\alpha(\mathbb{F}))_{/\equiv}, \models) \xrightarrow{\tilde{\alpha}} (FeatExp(\mathbb{F})_{/\equiv}, \models)$). The obtained Galois connections $(\tilde{\alpha}, \tilde{\gamma})$ are called dual (under-approximating) abstractions of (α, γ) .

The *dual join abstraction*, $\tilde{\alpha}^{join}$, merges the control flow of all variants, obtaining a single variant that includes only those executions that occur in all variants. Each feature expression ψ is replaced with *true* if all configurations from \mathbb{K} satisfy ψ . The abstraction and concretization functions between $FeatExp(\mathbb{F})$ and $FeatExp(\emptyset)$, forming a Galois connection, are defined as: $\tilde{\alpha}^{join} = \neg \circ \alpha^{join} \circ \neg$ and $\tilde{\gamma}^{join} = \neg \circ \gamma^{join} \circ \neg$, that is:

$$\tilde{\alpha}^{join}(\psi) = \begin{cases} true & \text{if } \forall k \in \mathbb{K}. k \models \psi \\ false & \text{otherwise} \end{cases} \quad (2)$$

$$\tilde{\gamma}^{join}(\psi) = \begin{cases} \bigwedge_{k \in 2^{\mathbb{F}} \setminus \mathbb{K}} (\neg k) & \text{if } \psi \text{ is } true \\ false & \text{if } \psi \text{ is } false \end{cases}$$

4.2 Abstract MTS

Given a Galois connection $(\alpha^{join}, \gamma^{join})$ defined on the level of feature expressions, we now define the abstraction of an FTS as an MTS with two transition relations: one (may) preserving universal properties and the other (must) existential properties. The may-transitions describe the behaviour that is possible, but not need be realized in the variants of the family, whereas the must-transitions describe behaviour that has to be present in any variant of the family.

Definition 5 Let $\mathcal{F} = (S, Act, trans, I, AP, L, \mathbb{F}, \mathbb{K}, \delta)$ be an FTS, we define its abstraction to be the MTS $\alpha^{join}(\mathcal{F}) = (S, Act, trans^{may}, trans^{must}, I, AP, L)$, where $trans^{may} = \{t \in trans \mid \alpha^{join}(\delta(t)) = true\}$, and also $trans^{must} = \{t \in trans \mid \alpha^{join}(\delta(t)) = true\}$.

Note that the abstract model $\alpha^{join}(\mathcal{F})$ has no variability in it, i.e. it contains only one abstract configuration (that is, $true \in \alpha^{join}(\mathbb{K})$).

Example 4 Recall the FTS VENDINGMACHINE of Fig. 1 with the set of valid configurations \mathbb{K}^{VM} (see Example 1). Figure 3 shows $\alpha^{join}(\text{VENDINGMACHINE})$, where the allowed (may) part of the behaviour includes the transitions that are associated with the optional features *c*, *f*, *t* in VENDINGMACHINE, whereas the required (must) part includes the transitions associated with the mandatory features *v* and *s*. Note that $\alpha^{join}(\text{VENDINGMACHINE})$ is an ordinary MTS with no variability. \square

From the MTS \mathcal{M} , we define two TSs \mathcal{M}^{may} and \mathcal{M}^{must} representing the may- and must-components of \mathcal{M} , i.e. they only contain may- and must-transitions of \mathcal{M} , respectively. Thus, we have $\llbracket \mathcal{M}^{may} \rrbracket_{TS} = \llbracket \mathcal{M} \rrbracket_{MTS}^{may}$ and $\llbracket \mathcal{M}^{must} \rrbracket_{TS} = \llbracket \mathcal{M} \rrbracket_{MTS}^{must}$.

4.3 Preservation of CTL*

We now show that the abstraction of an FTS is sound with respect to CTL*. First, we show two helper lemmas stating that: if any variant $k \in \mathbb{K}$ that can execute a behaviour, then the abstract model $\alpha^{join}(\mathcal{F})$ can execute the same may-behaviour; and if the abstract model $\alpha^{join}(\mathcal{F})$ can execute a must-behaviour, then all valid variants $k \in \mathbb{K}$ can execute the same behaviour.

Lemma 1 Let $\psi \in FeatExp(\mathbb{F})$, and \mathbb{K} be a set of valid configurations over \mathbb{F} .

- (i) Let $k \in \mathbb{K}$ and $k \models \psi$. Then, $\alpha^{join}(\psi) = true$.
- (ii) Let $\tilde{\alpha}^{join}(\psi) = true$. Then, for all $k \in \mathbb{K}$, it holds $k \models \psi$.

Proof (i) By assumption, we have that $\exists k \in \mathbb{K}. k \models \psi$. Thus, by definition of α^{join} in Eq. (1), we have $\alpha^{join}(\psi) = true$.

(ii) By assumption, we have that $\widetilde{\alpha}^{\text{join}}(\psi) = \text{true}$. By definition of $\widetilde{\alpha}^{\text{join}}$ in Eq. (2), this is the case only if for all $k \in \mathbb{K}$, it holds $k \models \psi$. \square

Lemma 2 (i) Let $k \in \mathbb{K}$ and $\rho \in \llbracket \pi_k(\mathcal{F}) \rrbracket_{\text{TS}}$. Then, $\rho \in \llbracket \alpha^{\text{join}}(\mathcal{F}) \rrbracket_{\text{MTS}}^{\text{may}}$.

(ii) Let $\rho \in \llbracket \alpha^{\text{join}}(\mathcal{F}) \rrbracket_{\text{MTS}}^{\text{must}}$. Then, $\rho \in \llbracket \pi_k(\mathcal{F}) \rrbracket_{\text{TS}}$ for all $k \in \mathbb{K}$.

Proof (i) Let $\rho = s_0 \lambda_1 s_1 \lambda_2 \dots \in \llbracket \pi_k(\mathcal{F}) \rrbracket_{\text{TS}}$ for some $k \in \mathbb{K}$. This means that for all transitions in ρ , $t_i = s_i \xrightarrow{\lambda_{i+1}} s_{i+1}$, we have that $k \models \delta(t_i)$ for all $i \geq 0$. By Lemma 1(i), we have that $\alpha^{\text{join}}(\delta(t_i)) = \text{true}$ for all $i \geq 0$. Hence, we have $t_i \in \text{trans}^{\text{may}}$ for $i \geq 0$, and so $\rho \in \llbracket \alpha^{\text{join}}(\mathcal{F}) \rrbracket_{\text{MTS}}^{\text{may}}$.
(ii) Let $\rho = s_0 \lambda_1 s_1 \lambda_2 \dots \in \llbracket \alpha^{\text{join}}(\mathcal{F}) \rrbracket_{\text{MTS}}^{\text{must}}$. This means that for all transitions in ρ , $t_i = s_i \xrightarrow{\lambda_{i+1}} s_{i+1}$, we have that $t_i \in \text{trans}^{\text{must}}$ and so $\alpha^{\text{join}}(\delta(t_i)) = \text{true}$ for all $i \geq 0$. By Lemma 1(ii), we have that for all $k \in \mathbb{K}$, it holds $k \models \delta(t_i)$ for all $i \geq 0$. Hence, we have $\rho \in \llbracket \pi_k(\mathcal{F}) \rrbracket_{\text{TS}}$ for all $k \in \mathbb{K}$. \square

As a result, every $\forall \text{CTL}^*$ (resp., $\exists \text{CTL}^*$) property true for the may- (resp., must-) component of $\alpha^{\text{join}}(\mathcal{F})$ is true for \mathcal{F} as well. Moreover, the MTS $\alpha^{\text{join}}(\mathcal{F})$ preserves the full CTL^* .

Theorem 1 (Preservation results) For any FTS \mathcal{F} , we have:

($\forall \text{CTL}^*$) For any $\Phi \in \forall \text{CTL}^*$, $\alpha^{\text{join}}(\mathcal{F})^{\text{may}} \models \Phi \implies \mathcal{F} \models \Phi$.

($\exists \text{CTL}^*$) For any $\Phi \in \exists \text{CTL}^*$, $\alpha^{\text{join}}(\mathcal{F})^{\text{must}} \models \Phi \implies \mathcal{F} \models \Phi$.

(CTL^*) For any $\Phi \in \text{CTL}^*$, $\alpha^{\text{join}}(\mathcal{F}) \models \Phi \implies \mathcal{F} \models \Phi$.

Proof We prove the most difficult case (CTL^*).

By induction on the structure of Φ . We prove for state formulae Φ that if $\alpha^{\text{join}}(\mathcal{F}) \models \Phi$, then $\mathcal{F} \models \Phi$ (i.e. for all $k \in \mathbb{K}$, $\pi_k(\mathcal{F}) \models \Phi$). All cases except \forall and \exists quantifiers are straightforward.

For $\Phi = \forall \phi$, we proceed by contraposition. Assume $\mathcal{F} \not\models \forall \phi$. Then, there exists a configuration $k \in \mathbb{K}$ and an execution $\rho \in \llbracket \pi_k(\mathcal{F}) \rrbracket_{\text{TS}}$ such that $\rho \not\models \phi$. By Lemma 2(i), we have that $\rho \in \llbracket \alpha^{\text{join}}(\mathcal{F}) \rrbracket_{\text{MTS}}^{\text{may}}$, and so $\alpha^{\text{join}}(\mathcal{F}) \not\models \forall \phi$.

For $\Phi = \exists \phi$. Assume $\alpha^{\text{join}}(\mathcal{F}) \models \exists \phi$. This means that there exists an execution $\rho \in \llbracket \alpha^{\text{join}}(\mathcal{F}) \rrbracket_{\text{MTS}}^{\text{must}}$ such that $\rho \models \phi$. By Lemma 2(ii), we have that for all $k \in \mathbb{K}$, we have $\rho \in \llbracket \pi_k(\mathcal{F}) \rrbracket_{\text{TS}}$, and so $\pi_k(\mathcal{F}) \models \exists \phi$. Since $\pi_k(\mathcal{F}) \models \exists \phi$ for all $k \in \mathbb{K}$, it follows $\mathcal{F} \models \exists \phi$. \square

The preservation results (soundness) from Theorem 1 mean that abstract models are designed to be conservative

for the satisfaction of CTL^* properties. However, in case of the refutation of a property, the counter-example found in the abstract model may be spurious (introduced due to abstraction) for some variants and genuine for the others. This can be established by checking which concrete variants can execute the found counter-example.

Let Φ be a CTL^* formula which is not in $\forall \text{CTL}^*$ nor in $\exists \text{CTL}^*$, and let \mathcal{M} be an MTS. We can verify $\mathcal{M} \models \Phi$ by checking Φ on two TSs \mathcal{M}^{may} and $\mathcal{M}^{\text{must}}$ and then by combining the obtained results as specified below.

Theorem 2 For any $\Phi \in \text{CTL}^*$ and MTS \mathcal{M} , we have:

$$\mathcal{M} \models \Phi = \begin{cases} \text{true} & \text{if } (\mathcal{M}^{\text{may}} \models \Phi \wedge \mathcal{M}^{\text{must}} \models \Phi) \\ \text{false} & \text{if } (\mathcal{M}^{\text{may}} \not\models \Phi \vee \mathcal{M}^{\text{must}} \not\models \Phi) \end{cases}$$

Proof By induction on the structure of Φ . All cases except \forall and \exists quantifiers are straightforward.

For $\Phi = \forall \phi$. Consider the first case, when $\mathcal{M} \models \Phi = \text{true}$. Assume $\mathcal{M}^{\text{may}} \models \forall \phi$. That is, for any may-execution ρ of \mathcal{M} we have $\rho \models \phi$. By Definition 4 (3'), we have $\mathcal{M} \models \Phi$. Consider the second case, when $\mathcal{M} \models \Phi = \text{false}$. Assume $\mathcal{M}^{\text{may}} \not\models \forall \phi$. That is, there exists a may-execution ρ of \mathcal{M} such that $\rho \not\models \phi$. By Definition 4 (3'), we have $\mathcal{M} \not\models \Phi$. Assume $\mathcal{M}^{\text{must}} \not\models \forall \phi$. That is, there exists a must-execution ρ of \mathcal{M} such that $\rho \not\models \phi$. But ρ is also a may-execution, so by Definition 4 (3'), we have $\mathcal{M} \not\models \Phi$.

For $\Phi = \exists \phi$. Consider the first case, when $\mathcal{M} \models \Phi = \text{true}$. Assume $\mathcal{M}^{\text{must}} \models \exists \phi$. That is, there exists a must-execution ρ of \mathcal{M} such that $\rho \models \phi$. By Definition 4 (3'), we have $\mathcal{M} \models \Phi$. Consider the second case, when $\mathcal{M} \models \Phi = \text{false}$. Assume $\mathcal{M}^{\text{may}} \not\models \exists \phi$. That is, for all may-executions ρ of \mathcal{M} we have $\rho \not\models \phi$. Since all must-executions are also may-executions, we have that all must-executions do not satisfy ϕ . By Definition 4 (3'), we have $\mathcal{M} \not\models \Phi$. Assume $\mathcal{M}^{\text{must}} \not\models \exists \phi$. That is, for all must-executions ρ of \mathcal{M} we have $\rho \not\models \phi$. By Definition 4 (3'), we have $\mathcal{M} \not\models \Phi$. \square

Therefore, we can check whether the abstract model $\alpha^{\text{join}}(\mathcal{F})$ satisfies a formula Φ , which is not in $\forall \text{CTL}^*$ nor in $\exists \text{CTL}^*$, by running a model checker twice, once with the may-component of $\alpha^{\text{join}}(\mathcal{F})$ and once with the must-component of $\alpha^{\text{join}}(\mathcal{F})$. On the other hand, a formula Φ from $\forall \text{CTL}^*$ (resp., $\exists \text{CTL}^*$) is checked against $\alpha^{\text{join}}(\mathcal{F})$ by running a model checker only once with the may-component (resp., must-component) of $\alpha^{\text{join}}(\mathcal{F})$.

Divide-and-conquer strategy The family-based model checking problem $\mathcal{F} \models \Phi$ can be reduced to a number of smaller problems by partitioning the configuration space \mathbb{K} . Let the subsets $\mathbb{K}_1, \mathbb{K}_2, \dots, \mathbb{K}_n$ form a partition of the set \mathbb{K} . Then, $\mathcal{F} \models \Phi$ iff $\pi_{\mathbb{K}_i}(\mathcal{F}) \models \Phi$ for all $i = 1, \dots, n$. By using Theorem 1 (CTL^*), we obtain the following result.

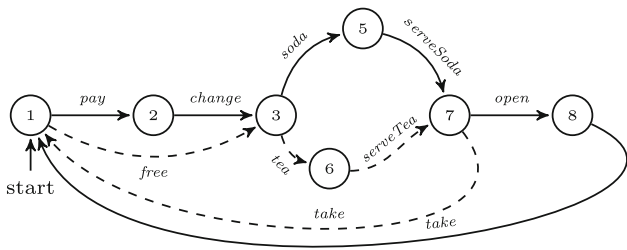


Fig. 4 $\alpha^{\text{join}}(\pi_{\llbracket \neg c \rrbracket})(\text{VENDINGMACHINE})$

Corollary 1 Let $\mathbb{K}_1, \mathbb{K}_2, \dots, \mathbb{K}_n$ form a partition of \mathbb{K} . If $\alpha^{\text{join}}(\pi_{\mathbb{K}_1}(\mathcal{F})) \models \Phi, \dots, \alpha^{\text{join}}(\pi_{\mathbb{K}_n}(\mathcal{F})) \models \Phi$, then $\mathcal{F} \models \Phi$.

Proof Assume $\alpha^{\text{join}}(\pi_{\mathbb{K}_1}(\mathcal{F})) \models \Phi, \dots, \alpha^{\text{join}}(\pi_{\mathbb{K}_n}(\mathcal{F})) \models \Phi$. By Theorem 1 (CTL*), it follows that $\pi_{\mathbb{K}_1}(\mathcal{F}) \models \Phi, \dots, \pi_{\mathbb{K}_n}(\mathcal{F}) \models \Phi$. Since $\mathbb{K}_1, \mathbb{K}_2, \dots, \mathbb{K}_n$ form a partition of \mathbb{K} , we have that $\pi_k(\mathcal{F}) \models \Phi$ for all $k \in \mathbb{K}$. Hence, $\mathcal{F} \models \Phi$. \square

Therefore, in case of suitable partitioning of \mathbb{K} and the aggressive α^{join} abstraction, all $\alpha^{\text{join}}(\pi_{\mathbb{K}_i}(\mathcal{F}))^{\text{may}}$ and $\alpha^{\text{join}}(\pi_{\mathbb{K}_i}(\mathcal{F}))^{\text{must}}$ are ordinary TSs, so the family-based model checking problem can be solved using existing single-system model checkers with all the optimizations that these tools may already implement.

Example 5 Consider the properties introduced in Example 2. We can verify $\Phi_1 = \forall \square \exists \diamond \text{start}$ by checking may- and must-components of $\alpha^{\text{join}}(\text{VENDINGMACHINE})$. In particular, we have $\alpha^{\text{join}}(\text{VENDINGMACHINE})^{\text{may}} \models \Phi_1$ and $\alpha^{\text{join}}(\text{VENDINGMACHINE})^{\text{must}} \models \Phi_1$. Thus, using Theorem 1, (CTL*) and Theorem 2, we have that $\text{VENDINGMACHINE} \models \Phi_1$.

Using the TS $\alpha^{\text{join}}(\text{VENDINGMACHINE})^{\text{may}}$, we can verify $\Phi_2 = \forall \square \forall \diamond \text{start}$ (Theorem 1, ($\forall \text{CTL}^*$)). In this case, we obtain the counter-example $1 \rightarrow 3 \rightarrow 5 \rightarrow 7 \rightarrow 3 \dots$, which is genuine for variants satisfying c . Hence, variants from $\llbracket c \rrbracket$ violate Φ_2 . On the other hand, we can establish that variants from $\llbracket \neg c \rrbracket$ satisfy Φ_2 in the following way. First, we can construct the model $\alpha^{\text{join}}(\pi_{\llbracket \neg c \rrbracket})(\text{VENDINGMACHINE})$, which is shown in Fig. 4. Since $\alpha^{\text{join}}(\pi_{\llbracket \neg c \rrbracket})(\text{VENDINGMACHINE})^{\text{may}}$ satisfies Φ_2 , we can conclude by Theorem 1, ($\forall \text{CTL}^*$) that all variants from $\llbracket \neg c \rrbracket$ satisfy Φ_2 .

Using $\alpha^{\text{join}}(\text{VENDINGMACHINE})^{\text{must}}$, we can verify $\Phi_3 = \exists \square \exists \diamond \text{start}$, by finding the witness $1 \rightarrow 2 \rightarrow 3 \rightarrow 5 \rightarrow 7 \rightarrow 8 \rightarrow 1 \dots$. By Theorem 1, ($\exists \text{CTL}^*$), we have that $\text{VENDINGMACHINE} \models \Phi_3$. \square

Example 6 Consider the property Φ_4 from Example 3. We can verify $\Phi_4 = \forall \diamond \square (\neg \text{selectedTea})$ using the TS $\alpha^{\text{join}}(\text{VENDINGMACHINE})^{\text{may}}$ (Theorem 1, ($\forall \text{CTL}^*$)). We obtain the counter-example $1 \rightarrow 3 \rightarrow 6 \rightarrow 7 \rightarrow$

$1 \dots$, which is genuine for variants satisfying t . Hence, variants from $\llbracket t \rrbracket$ violate Φ_4 . But, we can check that $\alpha^{\text{join}}(\pi_{\llbracket \neg t \rrbracket})(\text{VENDINGMACHINE})^{\text{may}} \models \Phi_4$, and thus by Theorem 1, ($\forall \text{CTL}^*$) it follows that all variants from $\llbracket \neg t \rrbracket$ satisfy Φ_4 . \square

5 Implementation

We now describe an implementation of our abstraction-based approach for CTL model checking of variational systems in the context of state-of-the-art NUSMV model checker [6]. FTSS do not represent a convenient formalism for modelling very large variational systems. Hence, it is much appreciated by engineers to use some high-level modelling languages for FTSS. We use a high-level modelling language, called fNUSMV, which is expressively equivalent to FTSS and close to NUSMV's input language. First, we introduce the core NUSMV language, and then we present its feature-aware extension, fNUSMV. Finally, we show how to implement projections and variability abstractions as syntactic source-to-source transformations of high-level models specified in fNUSMV.

5.1 NUSMV language

The NUSMV modelling language [6] represents a high-level syntax for describing finite state automata. A NUSMV model consists of list of modules with parameters, which can be used to encapsulate and factor out recurring sub-models. A module (MODULE) contains variable declarations (VAR), macrodefinitions (DEFINE), assignments (ASSIGN), and properties (SPEC) to be checked. The variable declarations define the state space and the assignments define the transition relation of the finite state automaton described by the given model. Possible types for variables are Booleans, finite ranges of integers, and enumerations. The assignments are of the form:

$$s(v) := \text{case } b_1 : e_1; \dots b_n : e_n; \text{esac} \tag{3}$$

where v is a variable, b_i is a Boolean expression, e_i is an expression (for $1 \leq i \leq n$), and $s(v)$ is one of v , $\text{init}(v)$, or $\text{next}(v)$. We use v to assign the current value of v , $\text{init}(v)$ to assign the initial value of v , and $\text{next}(v)$ to assign the value of v in the next state. The next state value of v is given as a function of the variable values in the current state. The case statement is evaluated top to bottom, so the result is the expression from the first branch e_i whose condition b_i evaluates to *true*. Note that all assignments in a model are evaluated in parallel.


```

1 MODULE features
2   VAR fA1 : boolean;
3   ASSIGN
4     init(fA1) := {TRUE,FALSE};
5     next(fA1) := fA1;
6 MODULE main
7   VAR f : features;
8     x : 0..1;
9     nA1 : boolean;
10  ASSIGN
11    init(x) := 0;
12    init(nA1) := {TRUE,FALSE};
13    next(x) := case f.fA1 & nA1 : x+1 mod 2;
14                TRUE : 0;
15                esac;
16    next(nA1) := case
17      f.fA1 & nA1 : next(x)=x+1?FALSE : nA1;
18      TRUE : nA1;
19      esac;
20  SPEC AF(x ≥ 0);
21  SPEC AG(EF(x = 0));
22  SPEC EG(x ≥ 0);

```

Fig. 7 The composed model M_1

posed with one feature, the meta-variable m is instantiated to 1. First, a module, called *features*, containing all features (in this case, the single one A_1) is added to the system. To each feature (e.g. A_1) corresponds one variable in this module (e.g. fA_1). The variable fA_1 is non-deterministically initialized to *TRUE* or *FALSE*, and its value never changes in next states. The *main* module contains a variable named f of type *features*, so that all feature variables can be referenced in it (e.g. $f.fA_1$). In the next state, the variable x is incremented by 1 when the feature A_1 is enabled (fA_1 is *TRUE*) and nA_1 holds. Otherwise (*TRUE*: can be read as *else*), x becomes zero. Also, nA_1 is set to *FALSE* when A_1 is enabled, nA_1 holds, and x is incremented by 1. Otherwise, nA_1 is not changed. The properties Φ_1 , Φ_2 , and Φ_3 with $k = 0$ hold for both variants when A_1 is enabled (fA_1 is *TRUE*) and A_1 is disabled (fA_1 is *FALSE*).

5.3 Syntactic transformations

We now show that projections and variability abstractions can be implemented as simple syntactic source-to-source transformations of fNUSMV models. This means that we do not need to build and store in memory the concrete full-blown FTS before applying an abstraction or projection to it, but we can effectively compute the abstract or projected model syntactically from the high-level modelling language. More specifically, let M be an fNUSMV model with a set of features \mathbb{F} and a set of configurations \mathbb{K} , and let $\llbracket M \rrbracket$ denote the FTS obtained by its compilation. We can define $\alpha^{\text{join}}(M)^{\text{may}}$ and $\alpha^{\text{join}}(M)^{\text{must}}$ as syntactic transformations, such that $\alpha^{\text{join}}(\llbracket M \rrbracket)_{\text{MTS}}^{\text{may}} = \llbracket \alpha^{\text{join}}(M)^{\text{may}} \rrbracket$ and $\alpha^{\text{join}}(\llbracket M \rrbracket)_{\text{MTS}}^{\text{must}} =$

$\llbracket \alpha^{\text{join}}(M)^{\text{must}} \rrbracket$. In other words, the may- (resp., must-) component of the abstract model obtained by applying α^{join} on the FTS $\llbracket M \rrbracket$, that is, $\alpha^{\text{join}}(\llbracket M \rrbracket)_{\text{MTS}}^{\text{may}}$ (resp., $\alpha^{\text{join}}(\llbracket M \rrbracket)_{\text{MTS}}^{\text{must}}$), coincides with the TS obtained by compiling the NUSMV model $\alpha^{\text{join}}(M)^{\text{may}}$ (resp., $\alpha^{\text{join}}(M)^{\text{must}}$), that is, the TS $\llbracket \alpha^{\text{join}}(M)^{\text{may}} \rrbracket$ (resp., $\llbracket \alpha^{\text{join}}(M)^{\text{must}} \rrbracket$). The same applies for projections $\pi_{\llbracket \psi \rrbracket}$.

We now describe the rewrites for projection and join abstraction in detail. Let $\mathbb{K}' \subseteq 2^{\mathbb{F}}$ be a set of configurations described by a feature expression ψ' , i.e. $\llbracket \psi' \rrbracket = \mathbb{K}'$. The projection $\pi_{\llbracket \psi' \rrbracket}(\llbracket M \rrbracket)$ is obtained by using the *INVAR* construct of NUSMV to add the feature expression ψ' as an invariant to the model M . Another solution is to add the feature expression ψ' to each condition b_i in the assignments [which are of the form in Eq. (3)] to the state variables.

The abstracts $\alpha^{\text{join}}(M)^{\text{may}}$ and $\alpha^{\text{join}}(M)^{\text{must}}$ are obtained as follows. Let $s(v) := rhs$ be an assignment in the composed model M of the form in Eq. (4), obtained from changes made by a feature A to a basic model. In $\alpha^{\text{join}}(M)^{\text{may}}$, if $\alpha^{\text{join}}(A) = true$ and $\alpha^{\text{join}}(\neg A) = true$ (that is, A is an optional feature), the above assignment becomes:

$$s(v) := \text{case } b : \{e, e'\}; TRUE : e'; \text{ esac} \quad (5)$$

When b is *true*, e or e' are non-deterministically assigned to $s(v)$. Otherwise, if A is a mandatory feature and $\alpha^{\text{join}}(\neg A) = false$, we have:

$$s(v) := \text{case } b : e; TRUE : e'; \text{ esac}$$

In $\alpha^{\text{join}}(M)^{\text{must}}$, if $\widetilde{\alpha^{\text{join}}(A)} = false$ and $\widetilde{\alpha^{\text{join}}(\neg A)} = false$ (that is, A is an optional feature), the above assignment becomes:

$$s(v) := \text{case } \neg b : e'; TRUE : v; \text{ esac}$$

Otherwise, if A is a mandatory feature and $\widetilde{\alpha^{\text{join}}(A)} = true$, we have:

$$s(v) := \text{case } b : e; TRUE : e'; \text{ esac}$$

For example, given the composed model M_1 in Fig. 7 and the optional feature A_1 , the models $\alpha^{\text{join}}(M_1)^{\text{may}}$ and $\alpha^{\text{join}}(M_1)^{\text{must}}$ are shown in Figs. 8 and 9, respectively. We can check that $\alpha^{\text{join}}(M_1)^{\text{may}}$ satisfies the properties Φ_1 and Φ_2 , whereas $\alpha^{\text{join}}(M_1)^{\text{must}}$ satisfies Φ_2 and Φ_3 .

Proposition 1 *Let M be a composed NUSMV model obtained using a basic model and features A_1, \dots, A_n . Then, we have $\alpha^{\text{join}}(\llbracket M \rrbracket)_{\text{MTS}}^{\text{may}} = \llbracket \alpha^{\text{join}}(M)^{\text{may}} \rrbracket$, and $\alpha^{\text{join}}(\llbracket M \rrbracket)_{\text{MTS}}^{\text{must}} = \llbracket \alpha^{\text{join}}(M)^{\text{must}} \rrbracket$.*

```

1 MODULE main
2   VAR x : 0..1;
3   nA1 : boolean;
4   ASSIGN
5     init(x) := 0;
6     init(nA1) := {TRUE, FALSE};
7     next(x) := case nA1 : {x + 1 mod 2, 0};
8         TRUE : 0;
9         esac;
10    next(nA1) := case
11      nA1 : {next(x) = x + 1 ? FALSE : nA1, nA1};
12      TRUE : nA1;
13      esac;
14  SPEC AF(x ≥ 0);
15  SPEC AG(EF(x = 0));

```

Fig. 8 The abstract model $\alpha^{\text{join}}(M_1)^{\text{may}}$

```

1 MODULE main
2   VAR x : 0..1;
3   nA1 : boolean;
4   ASSIGN
5     init(x) := 0;
6     init(nA1) := {TRUE, FALSE};
7     next(x) := case ¬nA1 : 0; TRUE : x; esac;
8     next(nA1) := nA1;
9   SPEC AG(EF(x = 0));
10  SPEC EG(x ≥ 0);

```

Fig. 9 The abstract model $\alpha^{\text{join}}(M_1)^{\text{must}}$

Proof It follows from the construction of $\llbracket M \rrbracket$, $\alpha^{\text{join}}(\llbracket M \rrbracket)$, $\alpha^{\text{join}}(M)^{\text{may}}$, and $\alpha^{\text{join}}(M)^{\text{must}}$. W.l.o.g., we assume that there is only one feature A .

Consider the case when A is an optional feature and in its CHANGE section, we have IF (b) THEN IMPOSE $s(v) := e$. Then, in $\llbracket M \rrbracket$ the assignment is given in Eq. (4), so in $\alpha^{\text{join}}(\llbracket M \rrbracket)_{\text{MTS}}^{\text{may}}$, $s(v)$ will be assigned non-deterministically to e or e' when b holds, and to e' otherwise. This is exactly what happens in $\llbracket \alpha^{\text{join}}(M)^{\text{may}} \rrbracket$ as shown in Eq. (5). The other cases are similar to show. \square

6 Evaluation

We evaluate our abstraction-based technique for verifying CTL properties of reactive variational systems. It consists of carefully devising projections of the configuration space, then applying the join abstraction on each of them, and verifying the obtained abstract models using the standard version of NUSMV. The evaluation aims to show that we can use state-of-the-art single-system model checkers to efficiently verify different variational systems using our technique. In order to do that, we ask the following research questions:

Table 1 Characteristics of our SPL benchmarks

Benchmark	$ \mathbb{F} $	$ \mathbb{K} $	LOC	STATES
Synthetic	20	2^{20}	200	$2^{41.81}$
ELEVATOR	9	2^9	300	2^{28}
TELEPHONE	7	2^7	700	$2^{48.45}$

RQ1: How efficient is our abstraction-based approach compared to the other approaches for verifying variational systems, such as family-based model checking and brute-force enumeration?

RQ2: Can the abstraction-based approach turn some previously infeasible verification tasks of variational systems into feasible ones?

6.1 Experimental setup

To evaluate our approach, we consider three case studies and a dozen of CTL properties. We use a synthetic example to demonstrate specific characteristics of our approach, as well as the ELEVATOR and TELEPHONE models which are standard benchmarks in the SPL community [4, 11, 16, 38]. Table 1 summarizes relevant characteristics for each benchmark: the number of features ($|\mathbb{F}|$), the number of valid configurations ($|\mathbb{K}|$), the number of lines of code (LOC), and the total number of reachable states in the compiled variability model. Note that we experiment with different versions of the synthetic example that have $|\mathbb{F}|$ ranging from 2 to 25, but we report in Table 1 the characteristics for the maximal version with $|\mathbb{F}| = 20$ that can be handled by the family-based version of NUSMV.¹

To establish our objectives, we analyse each case study with several properties. We compare: (1) our abstraction-based approach with the standard version of NUSMV as the verification tool versus (2) the plain family-based model checking approach with the family-based version of NUSMV [11] versus (3) the brute-force enumeration approach which verifies all variants one by one with the standard version of NUSMV. The reported performance numbers constitute the average runtime of five independent executions. For each experiment, we measure TIME which is the time to verify in seconds. We also report CALLS which is the number of times an approach calls NUSMV while performing a verification task. We say that a task is *infeasible* when it is taking more time than the given timeout threshold, which we set on 3 hours. The time computed for brute-force approach is the average of the all features enabled and the all features disabled configurations multiplied with the number of

¹ An extended version of NUSMV [11] implements the family-based algorithm for variability models obtained by composing the basic model and all available features.

```

1 MODULE features
2   VAR fA1, fA2 : boolean;
3   ASSIGN
4     init(fA1) := {TRUE, FALSE};
5     next(fA1) := fA1;
6     init(fA2) := {TRUE, FALSE};
7     next(fA2) := fA2;
8 MODULE main
9   VAR f : features;
10    x : 0..3;
11    nA1, nA2 : boolean;
12  ASSIGN
13    init(x) := 0;
14    init(nA1) := {TRUE, FALSE};
15    init(nA2) := {TRUE, FALSE};
16    next(x) := case
17      f.fA2 & nA2 : x+2 mod 4;
18      TRUE : case f.fA1 & nA1 : x+1 mod 4;
19                TRUE : x;
20      easc;
21    easc;
22  next(nA1) := case
23    f.fA1 & nA1 : next(x) = x+1?FALSE : nA1;
24    TRUE : nA1;
25  easc;
26  next(nA2) := case
27    f.fA2 & nA2 : next(x) = x+2?FALSE : nA2;
28    TRUE : nA1;
29  easc;

```

Fig. 10 The composed model M_2

valid configurations. The BDD model checker NUSMV is run with the parameter `-df -dynamic`, which ensures that the BDD package reorders the variables during verification in case the BDD size grows beyond a certain threshold.

All experiments were executed on a 64-bit Intel® Core™ i7-4600U CPU running at 2.10 GHz with 8 GB memory. The implementation, benchmarks, and all results obtained from our experiments are available from: <https://aleksdimovski.github.io/abstract-ctl.html>.

6.2 Synthetic example

As an experiment, we have tested limits of family-based model checking with extended (family-based) NUSMV and “brute-force” single-system model checking with standard NUSMV (where all variants are verified one by one). We have gradually added variability to the basic model in Fig. 5. This was done by adding optional features which increase the basic model’s variable x by the number corresponding to the given feature. In effect, the state space of resulting models M_n grows exponentially with the number of features, $n = |\mathbb{F}|$. Thus, for families with high variability, the verification tasks quickly become very prohibitive.

For example, the second feature A_2 introduces a new Boolean variable nA_2 , which is non-deterministically ini-

tialized. The CHANGE section for A_2 is:

```

IF (nA2) THEN IMPOSE next(x) := x+2
  mod (m+1); next(nA2) := next(x)
= x+2?FALSE : nA2

```

When the basic model in Fig. 5 is composed with two features A_1 and A_2 , the resulting model M_2 is shown in Fig. 10. Note that in this case the meta-variable m is instantiated to 3.

In Table 2, we compare the performances of checking the three CTL properties ($\Phi_1 = \forall \diamond (x \geq k)$, $\Phi_2 = \forall \square (\exists \diamond (x = k))$, and $\Phi_3 = \exists \square (x \geq k)$) using three different approaches: brute force, family based, and abstraction based, on this synthetic model when composed with different number of features. For $|\mathbb{F}| = 20$ (for which $|\mathbb{K}| = 2^{20}$ variants), the family-based NUSMV takes around 117 min to verify the above properties, using the state space of 2^{32} states, whereas for $|\mathbb{F}| > 20$ it has not finished the task within three hours. The analysis time to check the above properties using “brute force” with standard NUSMV ascends to almost 10 days for $|\mathbb{F}| = 20$, and to almost 2 years for $|\mathbb{F}| = 25$. On the other hand, if we apply the variability abstraction α^{join} , we are able to verify the above properties by only one call to standard NUSMV on the *abstract* model in 0.99 s for Φ_1 , 1.07 s for Φ_2 , and 0.12 s for Φ_3 when $|\mathbb{F}| = 20$, whereas it takes 133 s for Φ_1 , 159 s for Φ_2 , and 0.15 s for Φ_3 when $|\mathbb{F}| = 25$, thus effectively eliminating the exponential blowup (addresses **RQ1** and **RQ2**). The state space is around 2^{27} for the may-component and 2^{20} for the must-component of the abstract model when $|\mathbb{F}| = 20$, whereas 2^{33} for the may-component and 2^{25} for the must-component of the abstract model when $|\mathbb{F}| = 25$.

6.3 ELEVATOR

The ELEVATOR, designed by Plath and Ryan [38], contains about 300 LOC of NUSMV code and 9 independent optional features that modify the basic behaviour of the elevator. The features are: Antiprunk, Empty, Exec, OpenIfIdle, Overload, Park, QuickClose, Shuttle, and TTFull, thus yielding $2^9 = 512$ variants. The basic ELEVATOR system consists of a single lift that travels between five floors. It has three modules: *main*, *lift*, and *button*. The *main* module declares five platform buttons and a single lift, while the *lift* module declares variables *floor*, *door*, *direction*, and a further five cabin buttons. The *button* module contains a *pressed* variable, which is modelled non-deterministically, and a pressed button remains pressed until the lift has served the floor and its door opened. The lift will always serve all requests in its current direction before it stops and changes direction. When serving a floor, the lift door opens and closes again.

Table 2 Performance results for verifying synthetic models $M_{|\mathbb{F}|}$ using brute-force versus family-based versus abstraction-based approach

\mathbb{F}	BRUTE FORCE			FAMILY BASED			ABSTRACTION BASED		
	Φ_1	Φ_2	Φ_3	Φ_1	Φ_2	Φ_3	Φ_1	Φ_2	Φ_3
5	2.68	2.76	2.78	0.11	0.12	0.12	0.07	0.13	0.07
10	101.8	128.1	105.4	0.38	0.41	0.37	0.11	0.18	0.09
15	9175	9338	9080	71.8	75.6	71.6	0.25	0.36	0.10
20	Infeasible	Infeasible	Infeasible	7055	7312	7101	0.99	1.07	0.12
25	Infeasible	Infeasible	Infeasible	Infeasible	Infeasible	Infeasible	133	159	0.15

All times are in s (seconds)

Fig. 11 Verification of ELEVATOR properties using tailored abstractions. We compare brute-force versus family-based versus abstraction-based approach. All times are in sec (seconds)

prop-erty	BRUTE-FORCE		FAMILY-BASED		ABSTRACTION-BASED	
	CALLS	TIME	CALLS	TIME	CALLS	TIME
Φ_1	512	247.1	1	36.73	2	2.59
Φ_2	512	265.9	1	35.89	2	6.95
Φ_3	512	568.5	1	54.76	1	1.67
Φ_4	512	136.1	1	2.65	2	1.04
Φ_5	512	361.2	1	37.76	2	2.62

First, we consider two properties from \forall CTL. The property “ $\Phi_1 = \forall \square (floor = 2 \wedge liftBut5.pressed \wedge direction = up \Rightarrow \forall [direction = up \cup floor = 5])$ ” is that, when the elevator is on the second floor with direction up and the button five is pressed, then the elevator will go up until the fifth floor is reached. This property is violated by variants for which `Overload` (the elevator will refuse to close its doors when it is overloaded) is satisfied. Given sufficient knowledge of the system and the property, we can tailor an abstraction for verifying this property more effectively. We call standard NUSMV to check Φ_1 on two models $\alpha^{join}(\pi_{\llbracket Overload \rrbracket}(\text{ELEVATOR}))^{may}$ and $\alpha^{join}(\pi_{\llbracket \neg Overload \rrbracket}(\text{ELEVATOR}))^{may}$. For the first abstracted projection, we obtain an “abstract” counter-example violating Φ_1 , whereas the second abstracted projection satisfies Φ_1 . Similarly, we can verify that the \forall CTL property “ $\Phi_2 = \forall \square (floor = 2 \wedge direction = up \Rightarrow \forall \bigcirc (direction = up))$ ” is satisfied only by variants with enabled `Shuttle` (the lift will change direction at the first and last floor). We can successfully verify Φ_2 for $\alpha^{join}(\pi_{\llbracket Shuttle \rrbracket}(\text{ELEVATOR}))^{may}$ and obtain a counter-example for $\alpha^{join}(\pi_{\llbracket \neg Shuttle \rrbracket}(\text{ELEVATOR}))^{may}$.

Next, we consider a property from \exists CTL: “ $\Phi_3 = (OpenIfIdle \wedge \neg QuickClose) \Rightarrow \exists \diamond (\exists \square (door = open))$ ”, which states that there exists an execution such that from some state on the door stays open. The property is satisfied for variants where the feature `OpenIfIdle` (when idle, the lift opens its doors) is enabled and `QuickClose` (the lift door cannot be kept open by holding the platform buttons) is disabled. We can verify that Φ_3 holds for $\alpha^{join}(\pi_{\llbracket OpenIfIdle \wedge \neg QuickClose \rrbracket}(\text{ELEVATOR}))^{must}$.

The following two properties are neither in \forall CTL nor in \exists CTL. The property “ $\Phi_4 = \forall \square (floor = 1 \wedge idle \wedge door = closed \Rightarrow \exists \square (floor = 1 \wedge door = closed))$ ” is that for any execution globally, if the elevator is on the first floor, idle, and its door is closed, then there is a continuation where the elevator stays on the first floor with closed door. The satisfaction of Φ_4 can be established by verifying it against both $\alpha^{join}(\text{ELEVATOR})^{may}$ and $\alpha^{join}(\text{ELEVATOR})^{must}$ using two calls to standard NUSMV. The property “ $\Phi_5 = Park \Rightarrow \forall \square (floor = 1 \wedge idle \Rightarrow \exists [idle \cup floor = 1])$ ” is satisfied by all variants with enabled `Park` (when idle, the elevator returns to the first floor). We can successfully verify Φ_5 by analysing $\alpha^{join}(\pi_{\llbracket Park \rrbracket}(\text{ELEVATOR}))^{may}$ and $\alpha^{join}(\pi_{\llbracket Park \rrbracket}(\text{ELEVATOR}))^{must}$ using two calls to standard NUSMV.

The size of the family-based version of the ELEVATOR model is 2^{28} states. On the other hand, the sizes of $\alpha^{join}(\text{ELEVATOR})^{may}$ and $\alpha^{join}(\text{ELEVATOR})^{must}$ are 2^{20} and 2^{19} states, resp. We obtain the similar sizes for individual models used in the brute-force approach. We can see in Fig. 11 that abstractions achieve significant speed-ups between 2.5 and 32 times (resp., 38 and 340 times) faster than the family-based (resp., brute-force) approach (addresses **RQ1**).

6.4 TELEPHONE

The TELEPHONE variational system is initially designed by Plath and Ryan [38] and later extended by Ben-David et al. [4]. It contains about 700 LOC of NUSMV code and 7 independent optional features, thus yielding $2^7 = 128$ variants. The features are: `Call Forward on Busy` for

one phone (CFB-1), Call Forward on Busy for two phones (CFB-2), Call Forward on No Replay for one phone (CFNR-1), Call Forward on No Replay for all phones (AllCFNR), Terminating Call Screening for one phone (TCS-1), Ring Back When Free for one phone (RBWF-1), as well as Call Forward Unconditional for one phone (CFU-1).

The basic TELEPHONE system is a network of four synchronous phones, such that two are complete phones: one is terminating phone (it can only receive calls), and one is originating phone (it can only make calls). The module corresponding to each of the phones declares two variables st and $dialled$. Each phone can be found in one of the following states: $st \in \{idle, dialt, trying, busyt, ringingt, talking, ended, talked, ringing\}$. Initially, the phone is in the state *idle*, and from there it may move to *ringing* (if someone rings to it) or to *dialt* (if the phone is lifted to dial). The phone is in the state *talking* if it is in conversation which it initiated, whereas it is in the state *talked* if the phone is in conversation that was initiated by someone else. *Ended* means that the phone has hung up the conversation. The variable $dialled$ determines the other phone with which the given phone wants to establish a connection.

A desired property of the telephone system is: a phone can be talked to. That is, if we instantiate this property for the first phone, we have: “ $\Phi_1 = \exists \Diamond (ph_1.st = talked)$ ”. However, this property is violated by variants with enabled feature CFU-1 (all calls to the subscriber’s phone are diverted to another phone). Therefore, we can tailor an abstraction for verifying this \exists CTL property against two abstract models: $\alpha^{join}(\pi_{\llbracket CFU-1 \rrbracket}(\text{TELEPHONE}))^{must}$ which violates Φ_1 , and $\alpha^{join}(\pi_{\llbracket \neg CFU-1 \rrbracket}(\text{TELEPHONE}))^{must}$ which satisfies Φ_1 . The next property is “ $\Phi_2 = \forall \Box (\neg (ph_1.cfu - forw = 0) \implies \forall \Box \neg (ph_1.st \in \{ringing, talked\}))$ ”, which states that if a given phone has a forwarding number, then that phone will never ring. This \forall CTL property is violated by variants for which the feature CFU-1 is disabled. We call the standard NUSMV to check Φ_2 on two abstract models $\alpha^{join}(\pi_{\llbracket CFU-1 \rrbracket}(\text{TELEPHONE}))^{may}$ and $\alpha^{join}(\pi_{\llbracket \neg CFU-1 \rrbracket}(\text{TELEPHONE}))^{may}$. The first abstracted projection satisfies Φ_2 , while the second abstracted projection violates Φ_2 and a counter-example is reported.

We consider the property “ $\Phi_3 = \forall \Box ((ph_1.rbwf - number = 2 \wedge ph_1.st = talking \wedge ph_1.dialled = 2) \implies \forall \Diamond (ph_1.rbwf - number = 0))$ ”, in order to confirm the correctness of the feature RBWF (if we get a busy tone on calling another phone, there will be an attempt to establish a connection with that phone as soon as it becomes idle). The property Φ_3 states that the stored number will be reset when a call between a phone with RBWF feature on and the phone with the stored number is established. This property holds for all variants, and we can successfully verify it using our approach by checking $\alpha^{join}(\text{TELEPHONE})^{may}$. The prop-

erty “ $\Phi_4 = \forall \Box (ph_1.tcs3 \implies \forall \Box \neg (ph_3.dialled = 1 \wedge ph_3.st \in \{ringingt, talking\}))$ ” states that calls from numbers on the screening list are never accepted (the Boolean variable $tcs3$ is *true* when the phone 3 is on the screening list of the subscriber’s phone). This property is satisfied only by variants with enabled TCS-1 feature (calls to the subscriber’s phone from any number on its screening list will be rejected). We can successfully verify Φ_4 for $\alpha^{join}(\pi_{\llbracket TCS-1 \rrbracket}(\text{TELEPHONE}))^{may}$ and obtain a counter-example for $\alpha^{join}(\pi_{\llbracket \neg TCS-1 \rrbracket}(\text{TELEPHONE}))^{may}$.

The size of the family-based version of the TELEPHONE variability model is around 2^{48} states, whereas the sizes of $\alpha^{join}(\text{TELEPHONE})^{may}$, $\alpha^{join}(\text{TELEPHONE})^{must}$, as well as individual variants are around 2^{42} states. Figure 12 shows that abstraction-based approach achieves speed-ups between 1.1 and 11 times compared to the family-based approach, and between 15 and 390 times compared to the brute-force approach (addresses **RQ1**).

6.5 Threats to validity

Regarding internal validity, all experiments were executed five times on the same machine with all reported results averaged. The correctness of our abstraction-based approach was shown theoretically (Theorem 1, Theorem 2, and Proposition 1). For the correctness of the implementation, we rely on the results obtained from the family-based model checking approach and brute-force enumeration.

Regarding external validity, results on other case studies may differ and we cannot predict to what extent the obtained results can be generalized in such cases. However, we used benchmarks from published work and observed an improvement in efficiency for various interesting properties.

7 Future extensions

We now give an overview of two possible ways to extend our verification procedure in future. First, we may consider the richer set of temporal properties, as expressed in the modal μ -calculus [33]. Second, we may convert our procedure from manual (where a verification engineer decides what is the most suitable divide-and-conquer strategy for the given verification task) into fully automatic.

7.1 Modal μ -calculus properties

The modal μ -calculus logic [33], denoted L_μ , is a powerful temporal logic which is more expressive than CTL^* . L_μ formulae φ are defined by the following grammar:

$$\varphi ::= a \mid \neg a \mid x \mid \varphi_1 \wedge \varphi_2 \mid \varphi_1 \vee \varphi_2 \mid \Box \varphi \mid \Diamond \varphi \mid \mu x. \varphi \mid \nu x. \varphi$$

Fig. 12 Verification of TELEPHONE properties using tailored abstractions. We compare brute-force versus family-based versus abstraction-based approach. All times are in sec (seconds)

prop- erty	BRUTE-FORCE		FAMILY-BASED		ABSTRACTION-BASED	
	CALLS	TIME	CALLS	TIME	CALLS	TIME
Φ_1	128	53760	1	1566	2	138
Φ_2	128	55744	1	3876	2	3526
Φ_3	128	53614	1	1516	1	519
Φ_4	128	51072	1	2451	2	2358

where $x \in Var$ ranges over propositional variables. Note that L_μ formulae φ are given in negation normal form. The formula $\Box\varphi$ expresses that φ is true for every (immediate) successor, whereas $\Diamond\varphi$ expresses that there exists at least one successor for which φ is true. A propositional variable $x \in Var$ is a formula whose meaning (set of states in which it holds) depends on some environment $\rho : Var \rightarrow 2^S$ that binds variables to sets of states. The formula $\mu x.\varphi$ (resp., $\nu x.\varphi$) is the least (resp., greatest) fixpoint operator, which represents the smallest (resp., greatest) set x of states in which φ holds (where φ depends on x). The universal and existential fragments $\Box L_\mu$ and $\Diamond L_\mu$ are subsets of L_μ in which the only allowed next-state operators are \Box and \Diamond , respectively.

We now formalize the semantics $\llbracket \varphi \rrbracket_\rho^T$ of a L_μ formula φ over a TS \mathcal{T} and an environment $\rho : Var \rightarrow 2^S$, which specifies the interpretation of propositional variables.

Definition 6 The function $\llbracket \varphi \rrbracket_\rho^T : L_\mu \times (Var \rightarrow 2^S) \rightarrow 2^S$, which maps a formula φ to the set of states in which it holds, is defined as:

- (1) $\llbracket a \rrbracket_\rho^T = \{s \in S \mid a \in L(s)\}; \llbracket \neg a \rrbracket_\rho^T = \{s \in S \mid a \notin L(s)\}$
- (2) $\llbracket \varphi_1 \wedge \varphi_2 \rrbracket_\rho^T = \llbracket \varphi_1 \rrbracket_\rho^T \cap \llbracket \varphi_2 \rrbracket_\rho^T; \llbracket \varphi_1 \vee \varphi_2 \rrbracket_\rho^T = \llbracket \varphi_1 \rrbracket_\rho^T \cup \llbracket \varphi_2 \rrbracket_\rho^T$
- (3) $\llbracket \Box\varphi \rrbracket_\rho^T = \{s \in S \mid \forall s' \in S. (s, \lambda, s') \in trans \Rightarrow s' \in \llbracket \varphi \rrbracket_\rho^T\}$
 $\llbracket \Diamond\varphi \rrbracket_\rho^T = \{s \in S \mid \exists s' \in S. (s, \lambda, s') \in trans \wedge s' \in \llbracket \varphi \rrbracket_\rho^T\}$
- (4) $\llbracket \mu x.\varphi \rrbracket_\rho^T = \bigcap \{S' \subseteq S \mid \llbracket \varphi \rrbracket_{\rho[x \mapsto S']}^T \subseteq S'\}$
 $\llbracket \nu x.\varphi \rrbracket_\rho^T = \bigcap \{S' \subseteq S \mid S' \subseteq \llbracket \varphi \rrbracket_{\rho[x \mapsto S']}^T\}$

where $\rho[x \mapsto S']$ is the environment which is the same as ρ , except that x is mapped to S' . For a closed formula φ , we write $\mathcal{T}, s \models \varphi$ for $s \in \llbracket \varphi \rrbracket_{\perp_{env}}^T$, where \perp_{env} maps every $x \in Var$ to \emptyset . We write $\mathcal{T} \models \varphi$, iff all its initial states satisfy the formula: $\forall s_0 \in I. \mathcal{T}, s_0 \models \varphi$.

We say that an FTS \mathcal{F} satisfies a μ -calculus formula φ , written $\mathcal{F} \models \varphi$, iff all its valid variants satisfy the formula: $\forall k \in \mathbb{K}. \pi_k(\mathcal{F}) \models \varphi$.

The semantics of L_μ over an MTS \mathcal{M} is slightly different from the above Definition 6 where a TS \mathcal{T} is considered. In particular, the clause (3) is replaced by:

- (3') $\llbracket \Box\varphi \rrbracket_\rho^M = \{s \in S \mid \forall s'. (s, \lambda, s') \in trans^{may} \Rightarrow s' \in \llbracket \varphi \rrbracket_\rho^M\}$
 $\llbracket \Diamond\varphi \rrbracket_\rho^M = \{s \in S \mid \exists s'. (s, \lambda, s') \in trans^{must} \wedge s' \in \llbracket \varphi \rrbracket_\rho^M\}$

We then show that the MTS $\alpha^{join}(\mathcal{F})$ preserves the modal μ -calculus.

Theorem 3 (Preservation results) For any FTS \mathcal{F} :

- $(\Box L_\mu)$ For any $\varphi \in \Box L_\mu, \alpha^{join}(\mathcal{F})^{may} \models \varphi \Rightarrow \mathcal{F} \models \varphi$.
- $(\Diamond L_\mu)$ For any $\varphi \in \Diamond L_\mu, \alpha^{join}(\mathcal{F})^{must} \models \varphi \Rightarrow \mathcal{F} \models \varphi$.
- (L_μ) For any $\varphi \in L_\mu, \alpha^{join}(\mathcal{F}) \models \varphi \Rightarrow \mathcal{F} \models \varphi$.

Proof We prove the most general case (L_μ) .

By induction on the structure of φ . All cases except \Box and \Diamond next-operators are straightforward.

For $\varphi = \Box\varphi'$, we proceed by contraposition. Assume $\mathcal{F} \not\models \Box\varphi'$. Then, there exists a configuration $k \in \mathbb{K}$ and a transition $(s_0, \lambda, s_1) \in trans$ of $\pi_k(\mathcal{F})$ (where $s_0 \in I$ of $\pi_k(\mathcal{F})$), such that $s_1 \not\models \varphi'$. By Lemma 2(i), we have that $(s_0, \lambda, s_1) \in trans^{may}$ of $\alpha^{join}(\mathcal{F})$, and so $\alpha(\mathcal{F}) \not\models \Box\varphi'$.

For $\varphi = \Diamond\varphi'$. Assume $\alpha^{join}(\mathcal{F}) \models \Diamond\varphi'$. This means that there exists a must-transition $(s_0, \lambda, s_1) \in trans^{must}$ of $\alpha^{join}(\mathcal{F})$ (where $s_0 \in I$ of $\alpha^{join}(\mathcal{F})$), such that $s_1 \models \varphi'$. By Lemma 2(ii), we have for all $k \in \mathbb{K}, (s_0, \lambda, s_1) \in trans$ of $\pi_k(\mathcal{F})$, and so $\pi_k(\mathcal{F}) \models \Diamond\varphi'$. It follows $\mathcal{F} \models \Diamond\varphi'$. \square

The preservation result means that abstract models $\alpha^{join}(\mathcal{F})$ can be used to show validity of modal μ -calculus properties of concrete variational systems.

Similarly as for CTL, in future we can implement a verification procedure for modal μ -calculus. The implementation can be based on the general-purpose mCRL2 model checker, for which it has already been shown how to perform family-based model checking of modal μ -calculus properties [41].

7.2 An automatic verification procedure

We assume that a user of our approach has a good knowledge of the given variability model and property, so that he can manually devise suitable projections (partitionings) of the configuration space and variability abstractions before verification. We now give an overview of an algorithm, which aims to automate our verification approach so that the appropriate partitionings of the configuration space are constructed automatically. The algorithm is based on an abstraction and refinement framework for CTL* properties, which iteratively refines abstract variability models until either a genuine counter-example is found or the property

satisfaction is shown for all variants. In the heart of this algorithm are 3-valued model checking games [39,40], which represent the most suitable framework for defining the refinement. In particular, we use Shoham–Grumberg algorithm for solving such games, which is able to verify a CTL* property Φ on an MTS \mathcal{M} , that is, to check $\mathcal{M} \models \Phi$?. Since the 3-valued semantics of CTL* over MTSs is considered, there are three possible outcomes of the above check: (1) \mathcal{M} satisfies Φ ; (2) \mathcal{M} does not satisfy Φ ; and (3) an indefinite (don't know) result. In the case of an indefinite (don't know) result, the games framework [39,40] provides a way to find the failure reason for it which can be used for defining a refinement criterion. It splits abstract configurations so that the new, refined abstract configurations represent smaller subsets of concrete configurations. The sketch of the automatic abstraction-refinement procedure for checking $\mathcal{F} \models \Phi$, where \mathbb{K} is the set of configurations, is as follows:

1. Check by the algorithm for solving 3-valued model checking games whether $\alpha^{\text{join}}(\mathcal{F}) \models \Phi$?
2. If the result is *true*, Φ is satisfied by all variants in \mathbb{K} .
3. If the result is *false*, Φ is violated by all variants in \mathbb{K} .
4. Otherwise, an indefinite result is returned. Let the may-transition $s_1 \xrightarrow{\lambda} s_2$ in $\alpha^{\text{join}}(\mathcal{F})$ be the reason for failure (as identified by the game-based model checking algorithm), and let ψ be the feature expression guarding this transition in \mathcal{F} . We split the configuration set \mathbb{K} into two subsets $\mathbb{K} \cap \llbracket \psi \rrbracket$ and $\mathbb{K} \cap \llbracket \neg\psi \rrbracket$. We go back to Step (1), to check $\pi_{\llbracket \psi \rrbracket}(\mathcal{F}) \models \Phi$ with the set of configurations $\mathbb{K} \cap \llbracket \psi \rrbracket$, and to check $\pi_{\llbracket \neg\psi \rrbracket}(\mathcal{F}) \models \Phi$ with the set of configurations $\mathbb{K} \cap \llbracket \neg\psi \rrbracket$.

In future, we can develop more precisely and implement such an automatic abstraction-refinement procedure for verifying CTL* properties of variational systems. In this way, we will establish a brand new connection between games and SPL communities.

8 Related work

Many family-based analysis and verification techniques that work on the level of variational systems and programs have been proposed in recent years (see [43] for survey). Some successful examples range from family-based syntax and type checking [27,31,32], to family-based static analysis [5,19–22,37] and family-based verification by simulation [29,30,44]. Family-based model checking has also been an active research field, where different approaches have been developed for verifying variational systems.

One of the earliest attempts for modelling variability and variational systems is by using modal transition systems (MTSs) [34,42], where optional ‘may’-transitions are used

to model variability. In contrast, here we use MTSs with an entirely different goal of abstracting variational systems, which is closer to the original idea of introducing MTSs by Larsen and Thomsen [35] (abstraction in system modelling and verification). Beek et al. [42] have implemented a model checking tool, called VMC, for verifying variability models expressed as MTSs and properties expressed as v-CTL formulae. They use MTSs as a compact representation of a family of Labelled TSSs, and use variability-aware action-based CTL logic to reason over MTSs. The preservation result in [42] shows that if a property holds over an MTS, then the same property holds for all variants derived from that MTS. In contrast, here we use MTSs to represent abstract models of a family of systems, and our preservation result (Theorem 1) states that if a property holds over an abstract model, then it holds over all variants derived from the concrete family. Subsequently, various variability models have been developed. Ultimately, the popular feature transition systems (FTSSs) have been introduced by Classen et al. [10], which are today widely accepted as models essentially sufficient for most purposes of family-based model checking of variational systems.

Firstly, Classen et al. [9] have presented specifically designed (explicit) family-based model checking algorithms for verifying FTSSs against LTL properties, which are implemented in the $\overline{\text{SNIP}}$ model checker. Then, (symbolic) family-based model checking algorithms [8,11] have been proposed to enable verification of FTSSs against CTL properties, which are implemented as an extension of the BDD model checker NuSMV. It uses symbolic encoding for FTSSs as well as symbolic algorithms for their verification. One of the most prominent methods to make all these approaches based on FTSSs more scalable to larger systems is to apply abstractions. In this work, we show how to construct abstract models of FTSSs that preserve CTL* properties. For implementation, we use the standard version of the BDD model checker NuSMV, where abstract models are symbolically encoded and its symbolic algorithms are used for model checking.

Simulation-based abstractions on FTSSs introduced in [13,14] are defined by using existential F-abstraction functions, and simulation relation is used to relate different abstraction levels. Different levels of precision of so-called feature abstractions in [14] are defined by simply enriching (resp., reducing) the sets of variants for which transitions are enabled. These abstractions are applied either on concrete FTSSs [13], or on an intermediate concrete semantic model (called featured program graph) [14]. Therefore, they report smaller efficiency gains. For example, the approach [13] results in marginal efficiency reductions of verification times of 8–9 % compared to the unabstracted approach. On the other hand, our variability abstractions defined as Galois connections are capable to change not only the feature expression labels of transitions but also the sets of available

features and valid configurations. Moreover, we can also use projection, which partitions the configuration space, in order to build various more sophisticated verification strategies. We apply our abstractions as preprocessor transformations directly on high-level modelling languages, thus avoiding to generate and store any concrete model in the memory. Therefore, we report significant performance speed-ups compared to the unabstracted approach.

All the previous abstractions applied on FTSS [13,14,17,18,24] are conservative, and thus, they construct over-approximated abstract models that preserve satisfiability only of LTL and universal \forall CTL properties. To our knowledge, in this work, for the first time, we propose abstractions on FTSS that preserve all CTL* (and μ -calculus), thus significantly extending the previous works on abstractions of FTSS.

The abstraction and refinement procedure for automatic verification of LTL properties of variational systems has been developed in [25]. If a spurious counter-example (introduced due to the abstraction) is found in the abstract model, the procedure [25] uses Craig interpolation to extract relevant information from it in order to define the refinement of abstract models. As we noted in Sect. 7, in the context of CTL* properties, the 3-valued model checking games proposed by Shoham and Grumberg [39,40] represent the most suitable framework to define the refinement [23].

Verifying variability models against properties specified in modal μ -calculus has been also an interesting topic of research. Some examples are model checking algorithms for variability models specified in PL-CCS and Delta-CCS. PL-CCS [28] is an extension of Milner's process algebra CCS, which is enriched with a variant operator as a means to implement variability. Delta-CCS [36] is another delta-oriented extension of CCS, in which variability is achieved by decomposing the product line into a core process and a set of delta modules that encapsulate change directives based on term rewriting semantics. Beek et. al. [41] have also presented an approach for family-based model checking of modal μ -calculus properties using the general-purpose mCRL2 model checker. In this work, we show that the resulting abstract models also preserve the full μ -calculus properties, thus enabling our approach to be used for their verification as well.

Another approach to efficiently verify variational systems is by using variability encoding [2,30], which transforms features into non-deterministically initialized variables (replaces compile time with runtime variability). Then, the generated *family simulator* is verified using the standard single-system model checkers. However, in case of violation, the (single-system) model checker stops after a single counter-example and a violating variant are found. Therefore, this answer is incomplete (limited) since there might be other satisfying variants and also there might be other violating variants with different counter-examples. In contrast,

family-based model checking and our approach provide precise conclusive results for all variants in the family.

9 Conclusion

We have proposed conservative (over-approximating) and their dual (under-approximating) variability abstractions to derive abstract family-based model checking that preserves the full CTL*. The projections and abstractions used in our divide-and-conquer verification procedure are implemented as source-to-source transformations of high-level fNUSMV variability models. The evaluation confirms that various CTL properties can be efficiently verified in this way.

In this work, we focus on the state-based approach for analysing variational systems. This means that we abstract from actions, and only use atomic propositions of the states to formulate system properties. A combined action- and state-based approach is possible, but leads to more involved definitions and concepts. Moreover, NuSMV model checker used in the implementation is also based exclusively on the state-based approach.

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